

Fig. 1

PRIOR ART

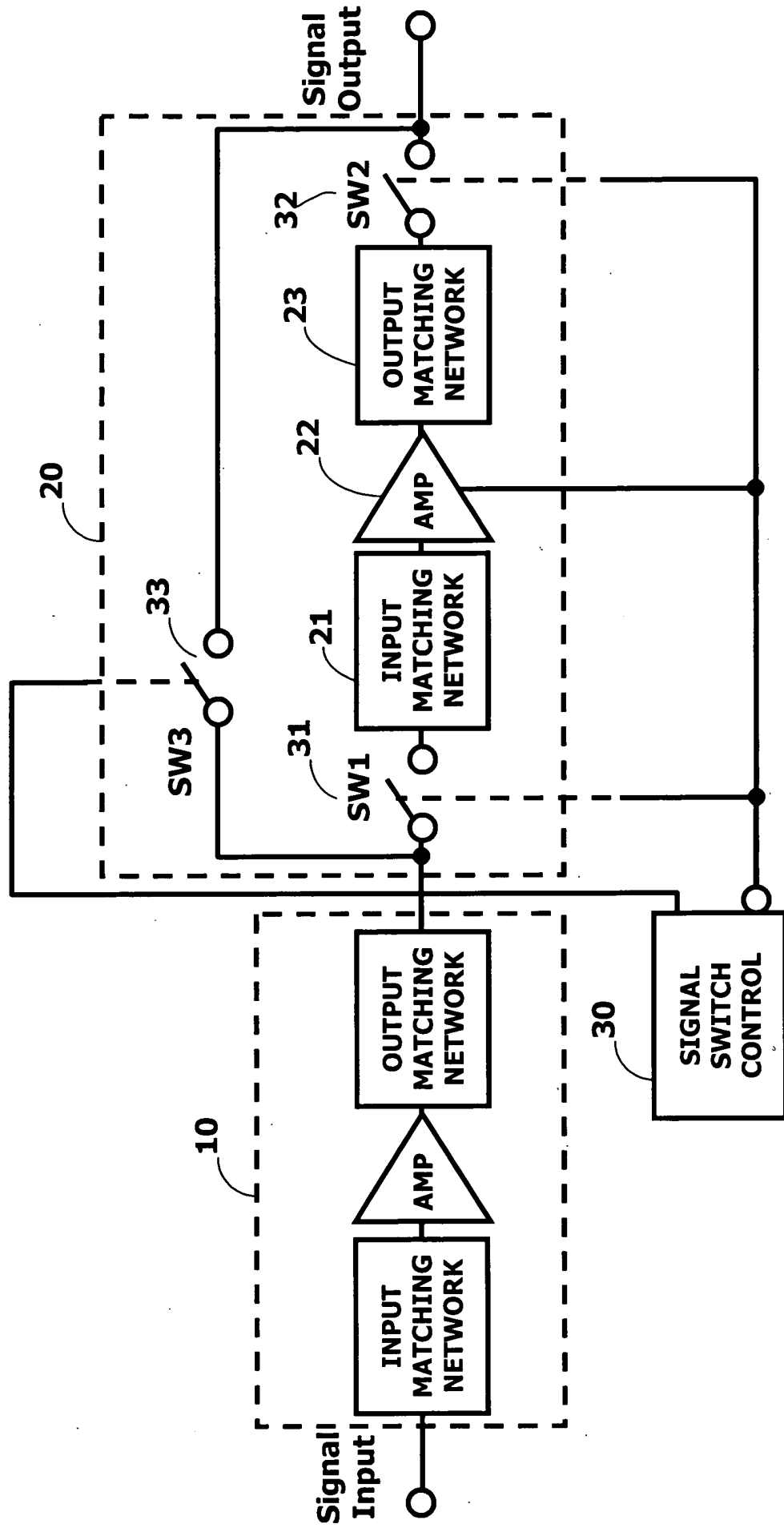
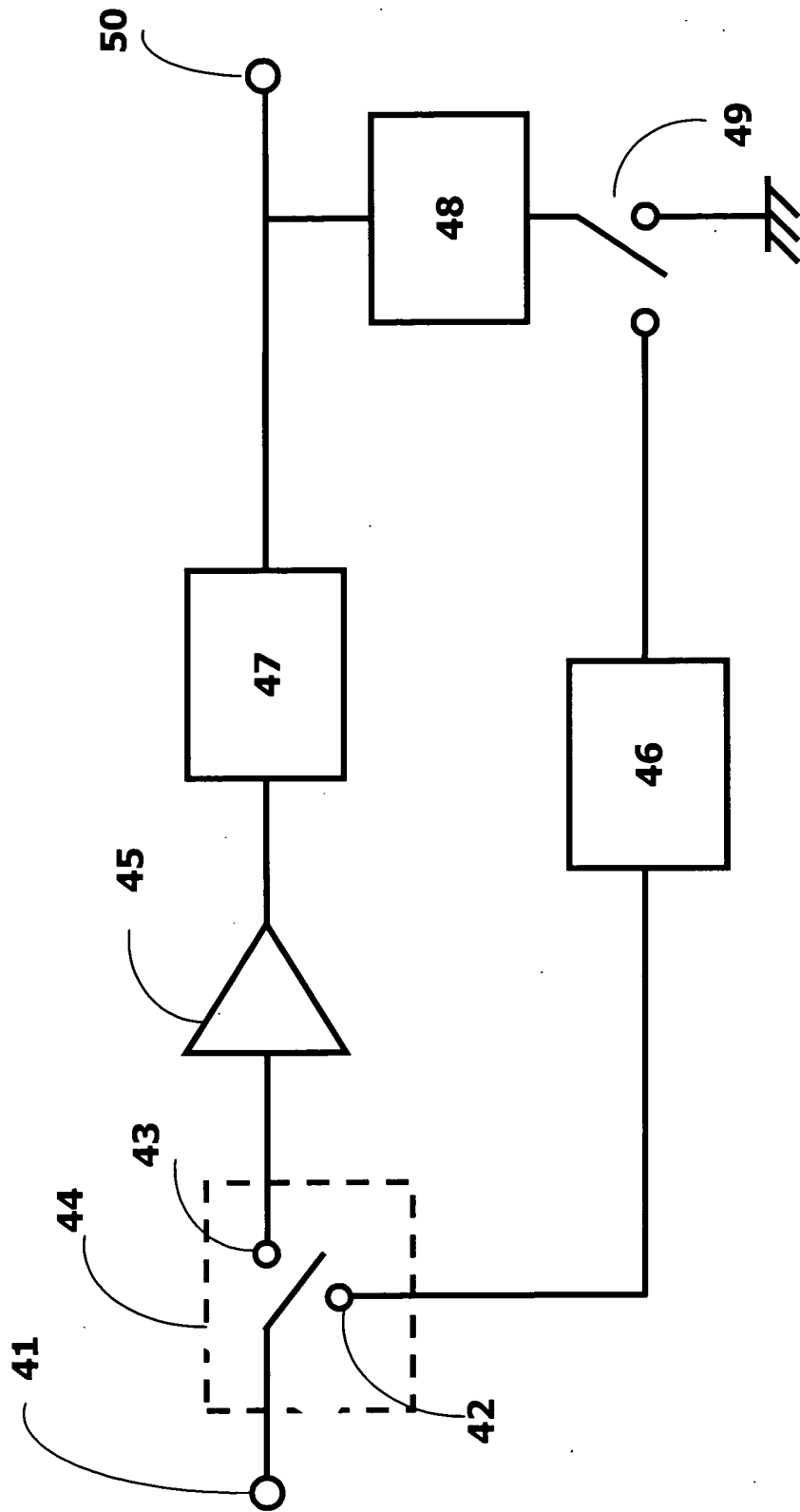


Fig. 2

PRIOR ART



PRIOR ART

Fig. 3A

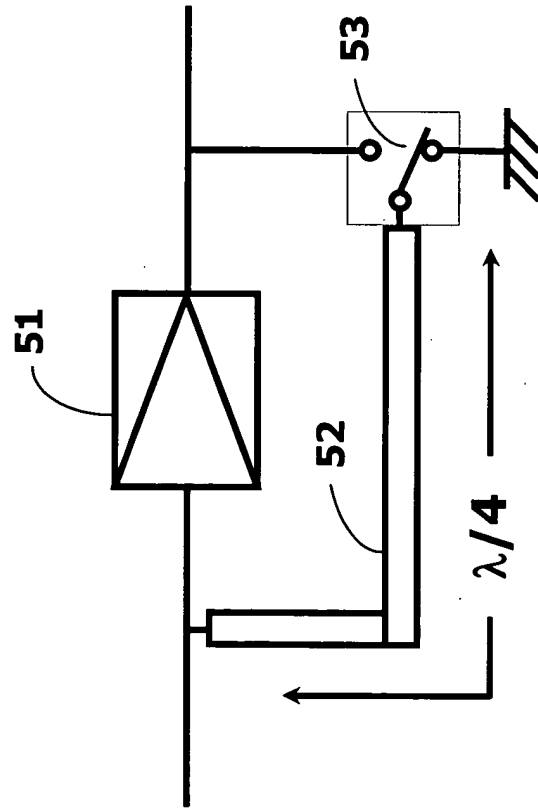
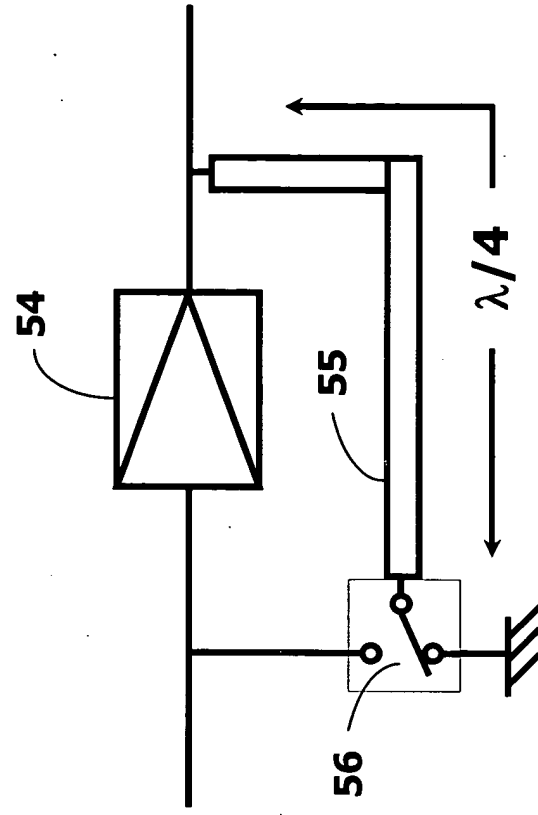


Fig. 3B



PRIOR ART

Fig. 4

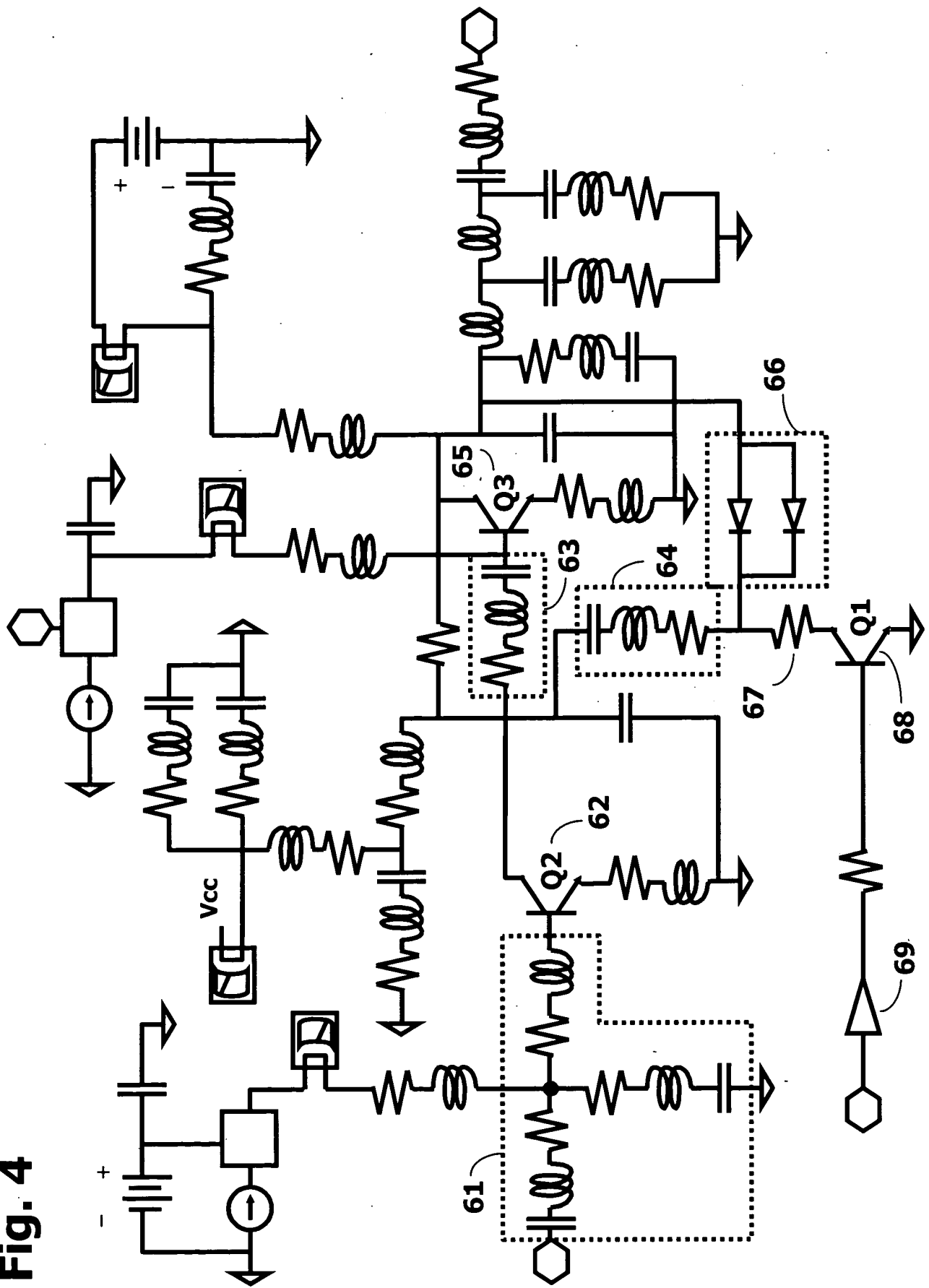


Fig. 5

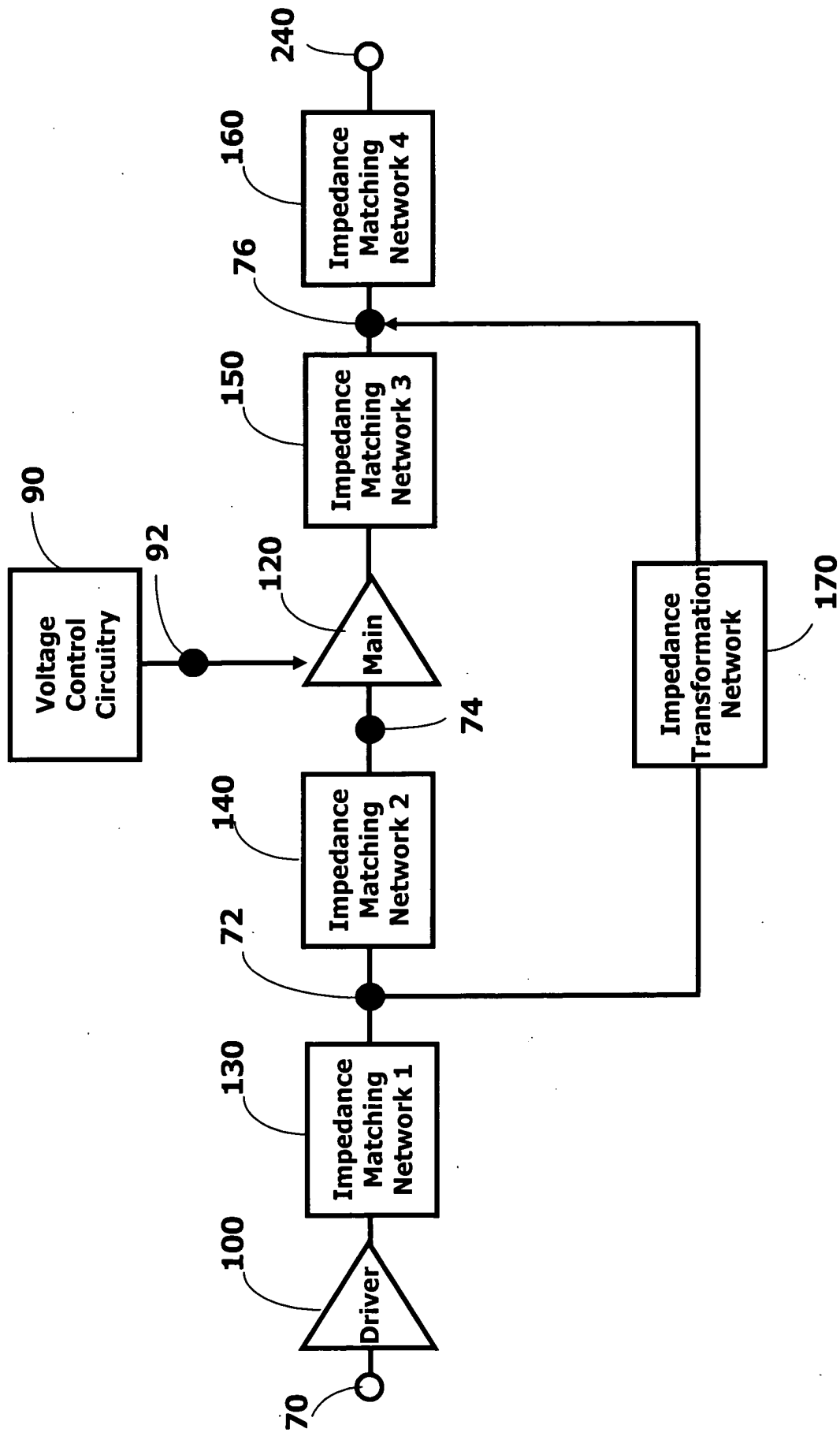


Fig. 6

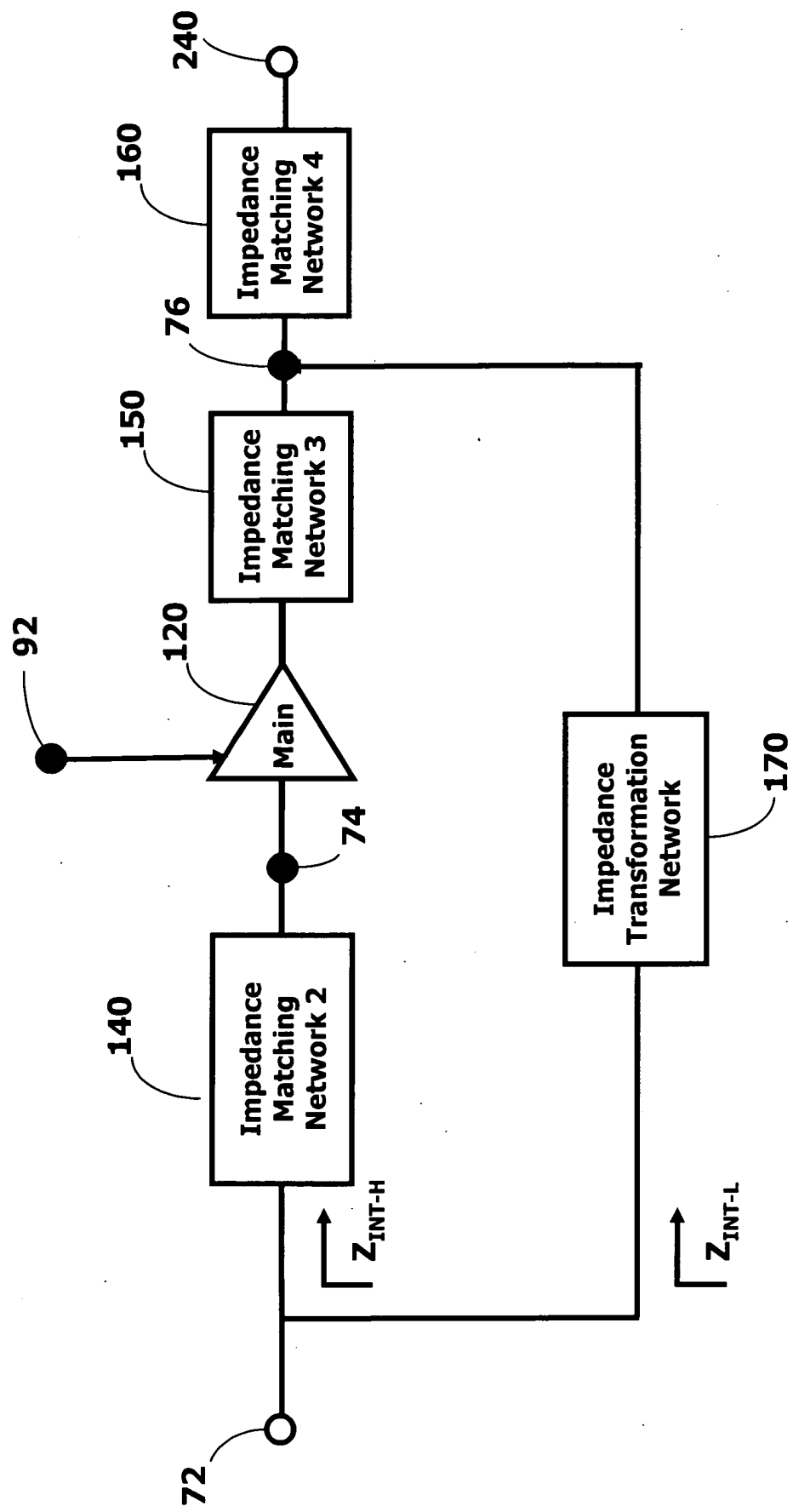


Fig. 7A

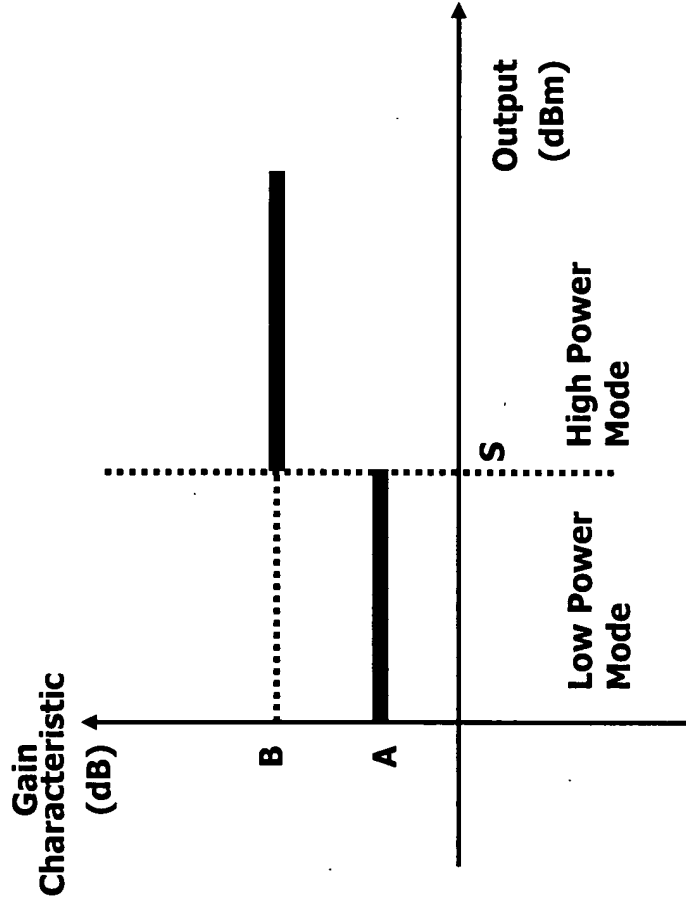


Fig. 7B

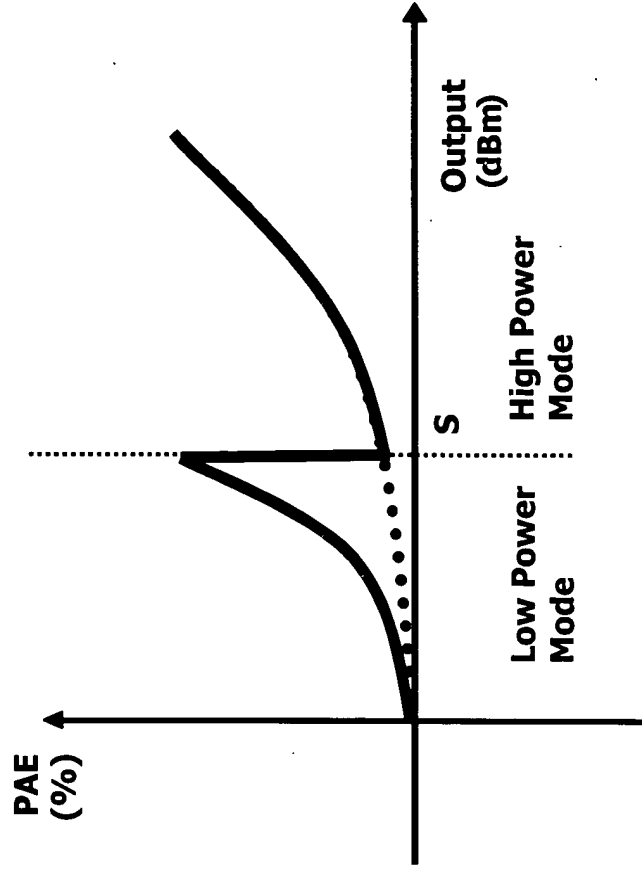


Fig. 8

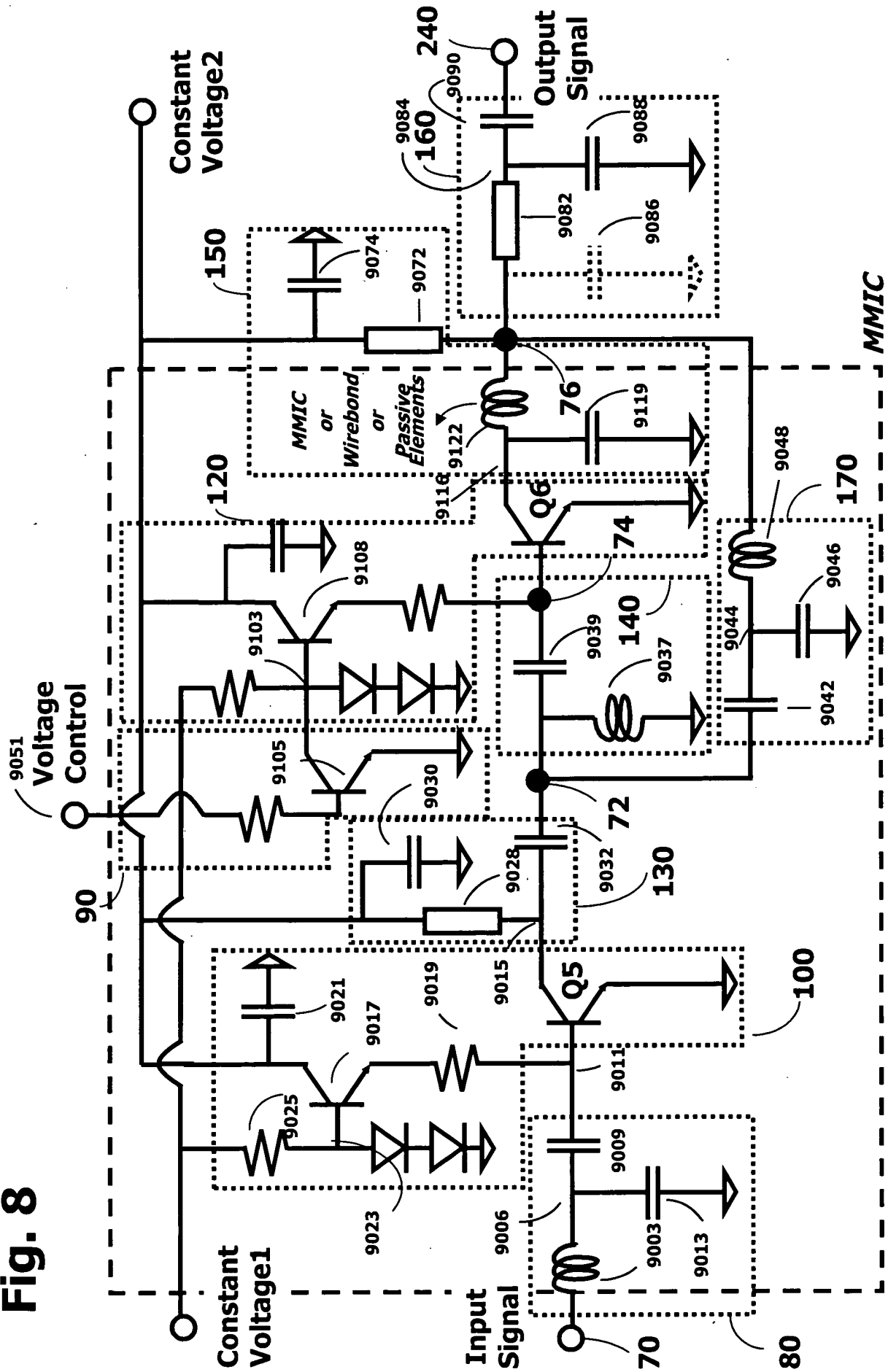


Fig. 9

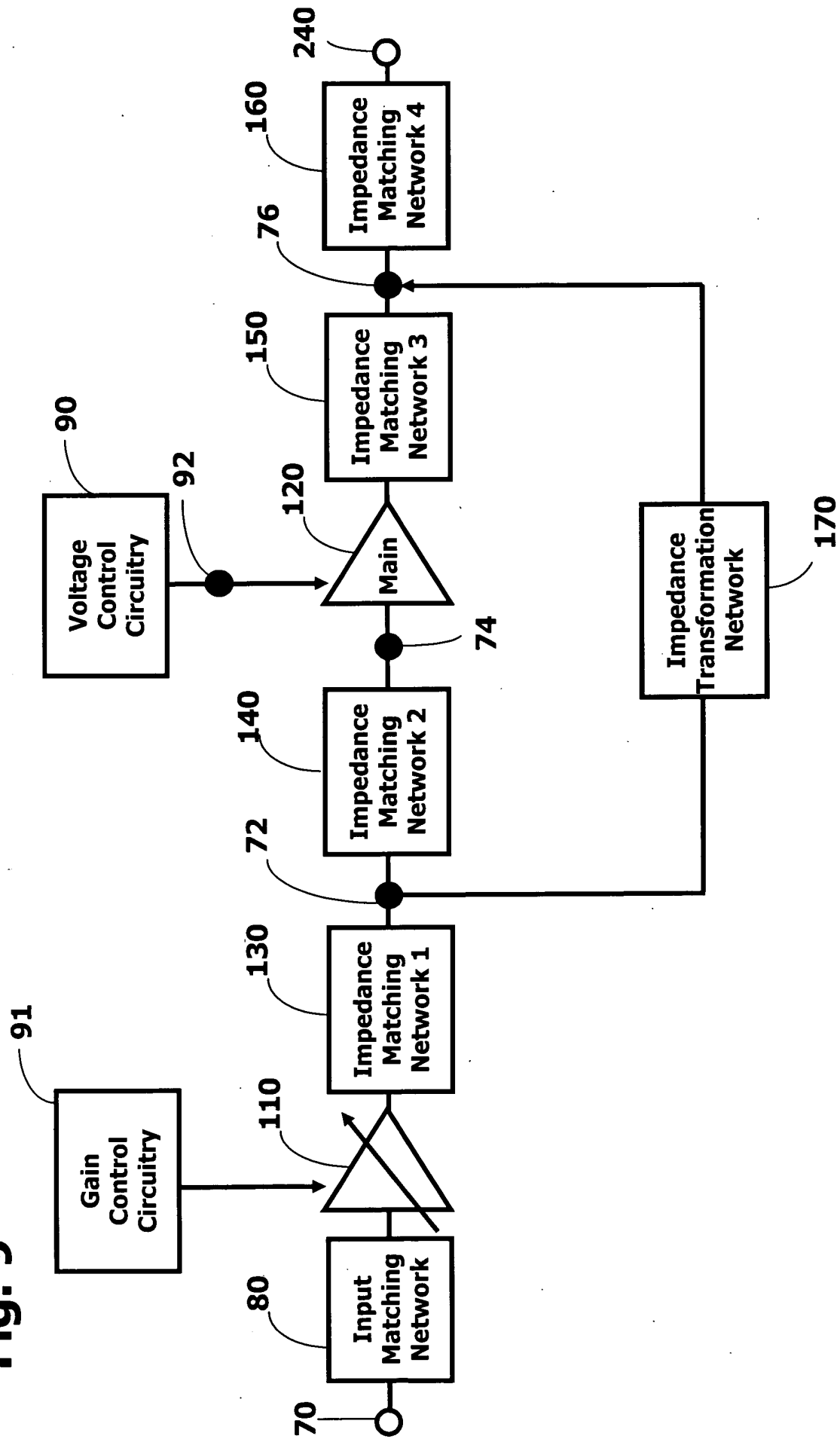


Fig. 10

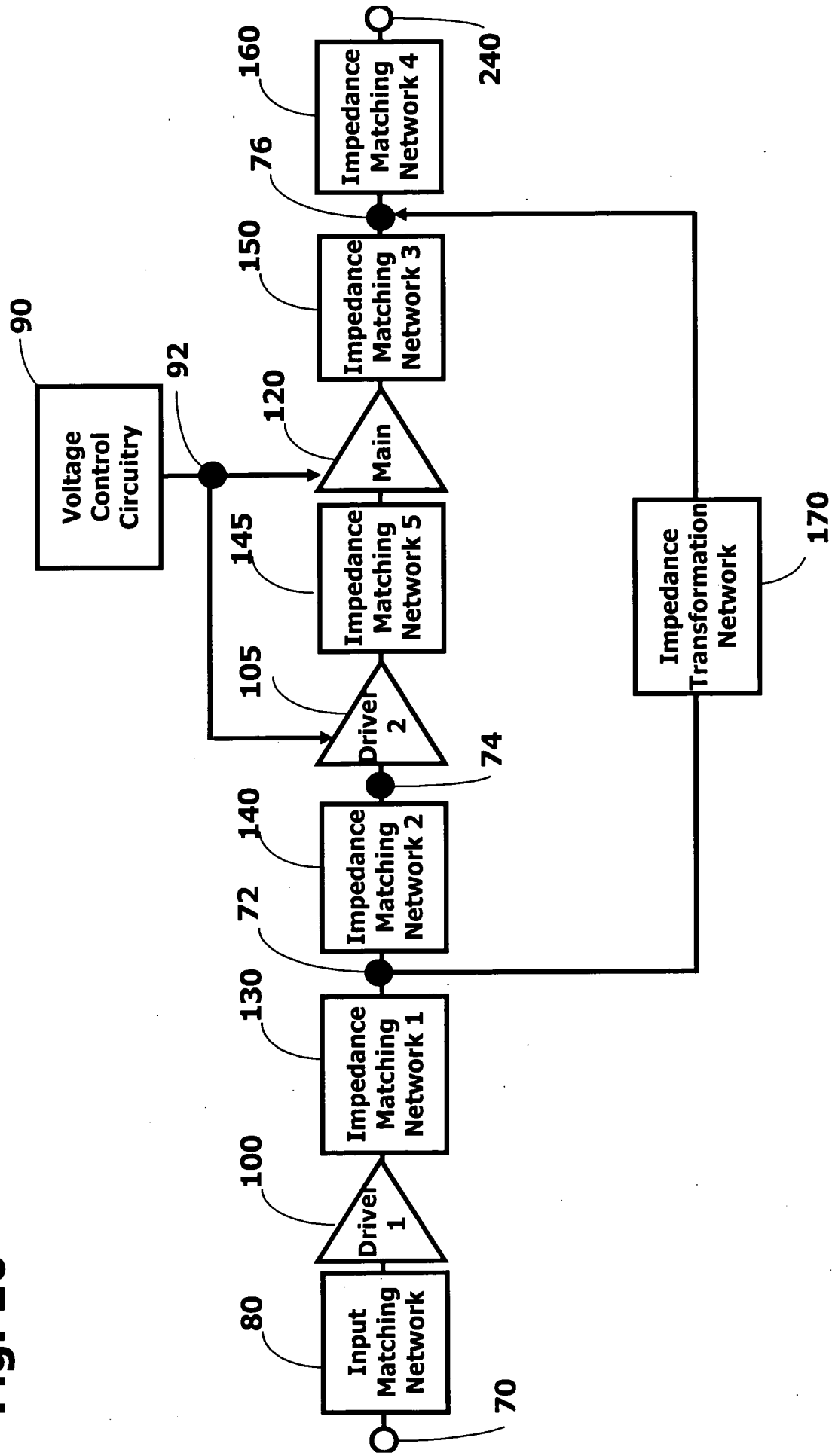


Fig. 11

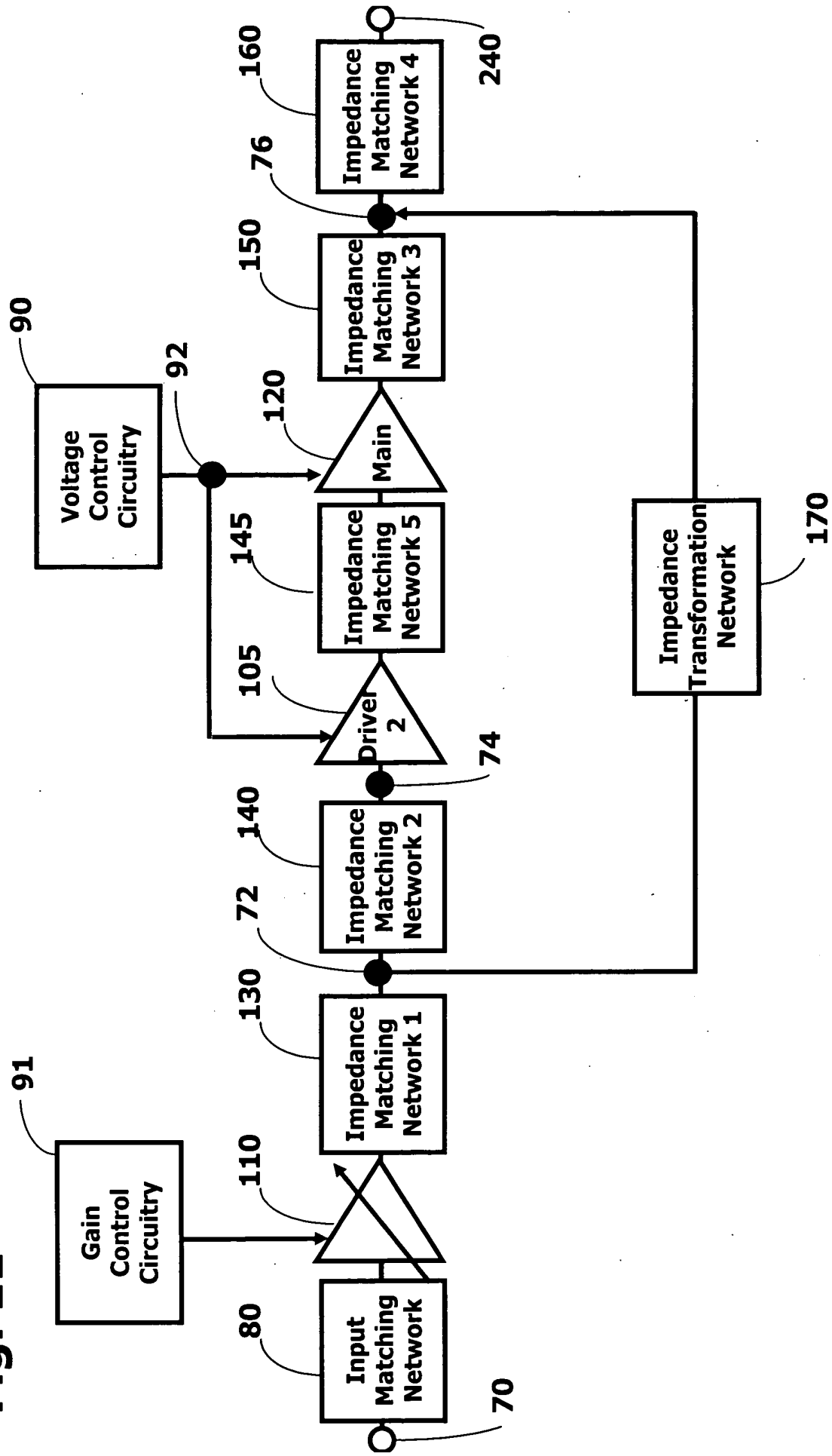


Fig. 12

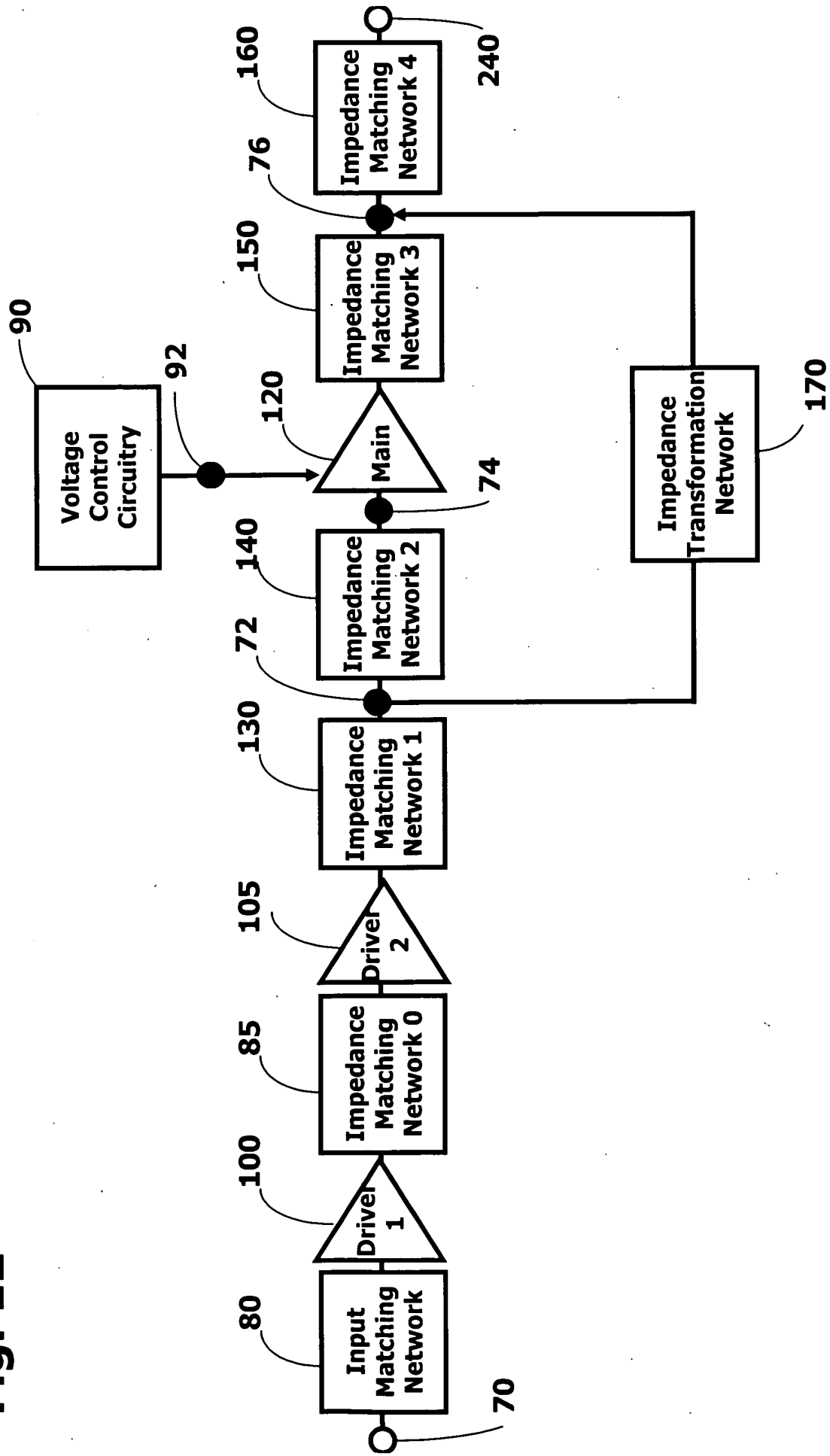


Fig. 13

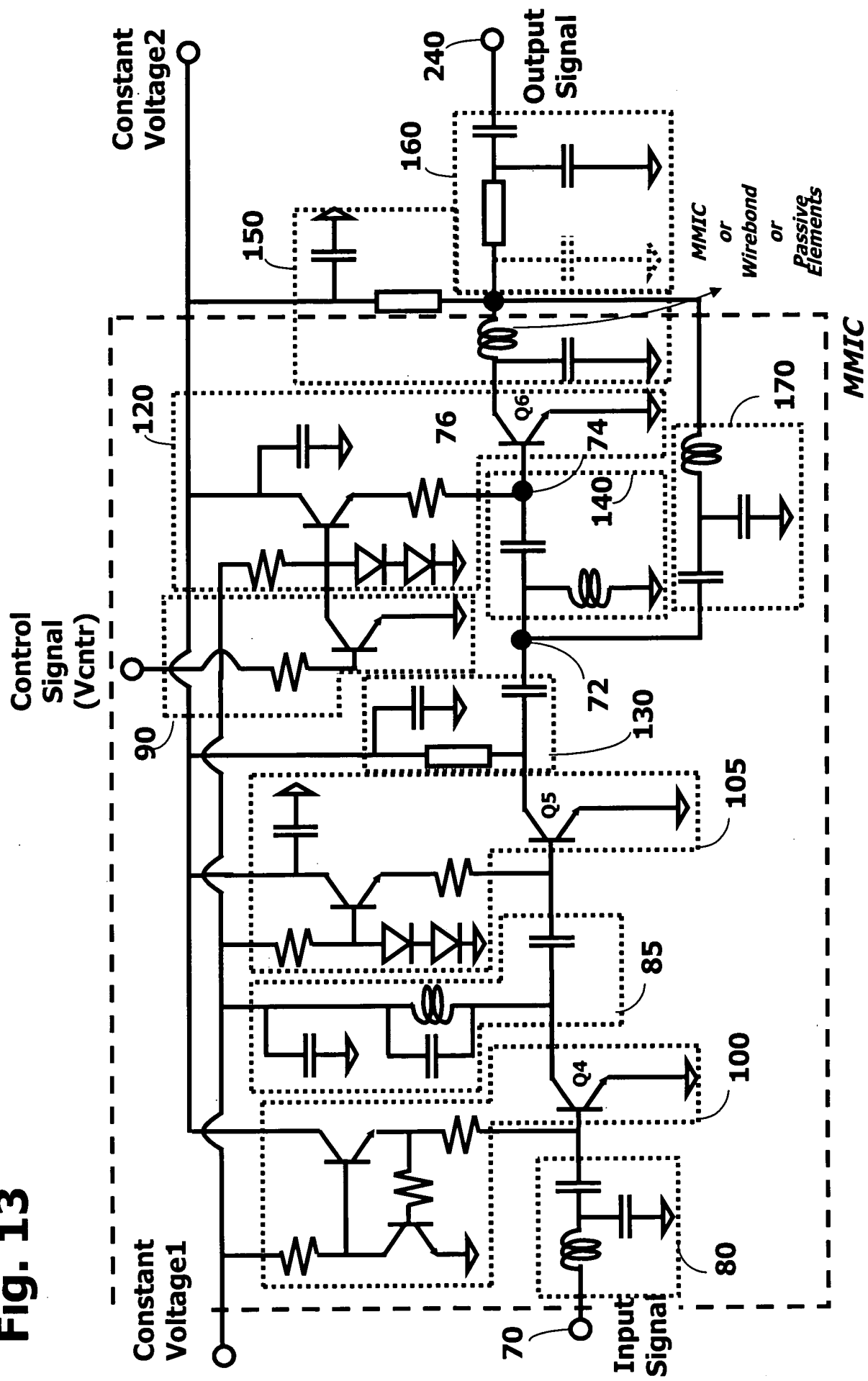
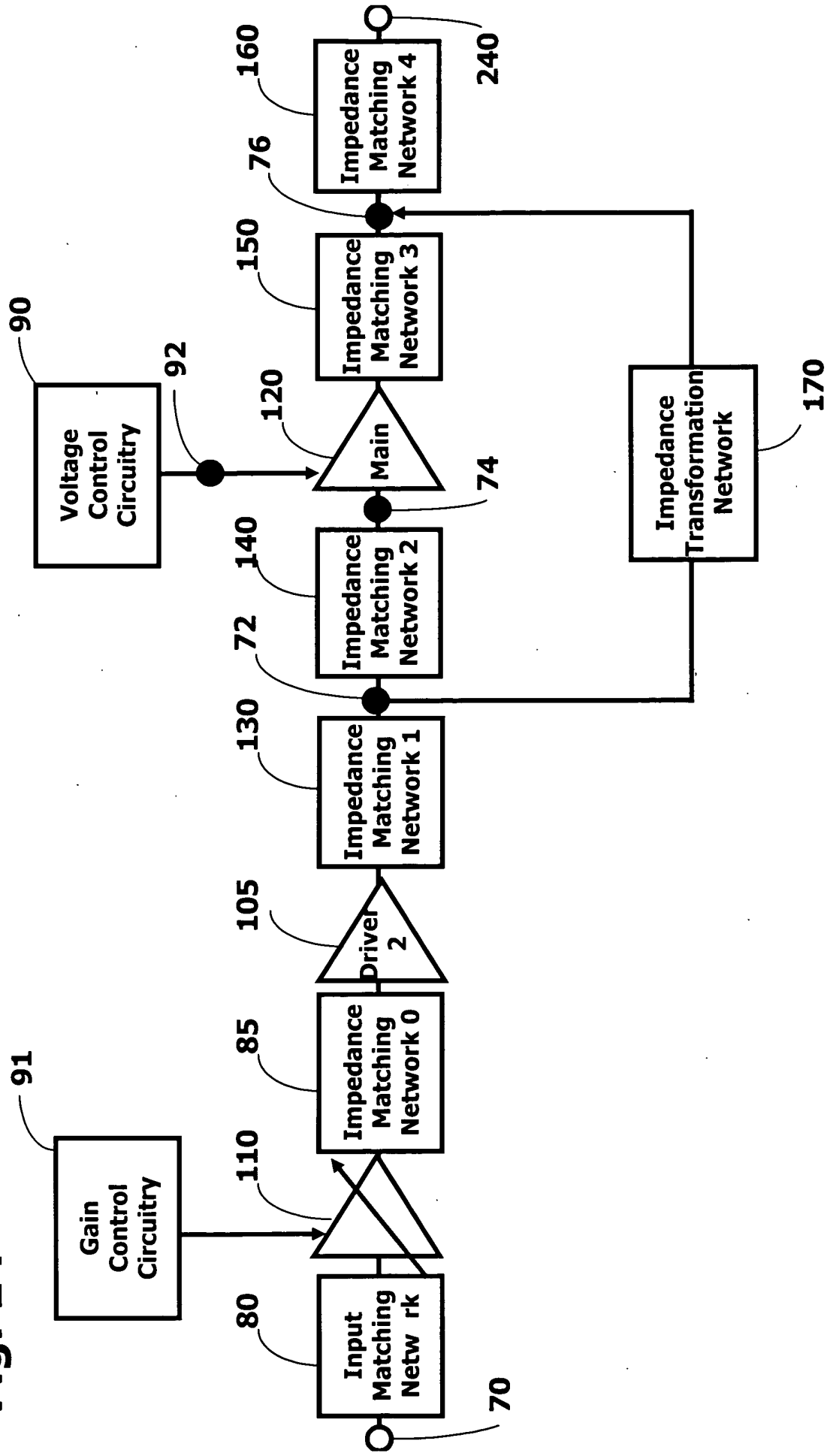


Fig. 14



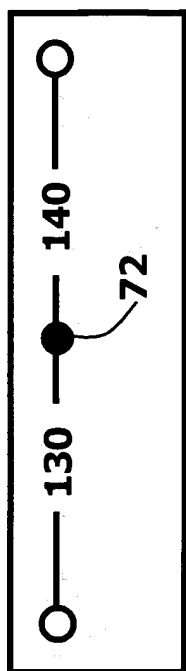
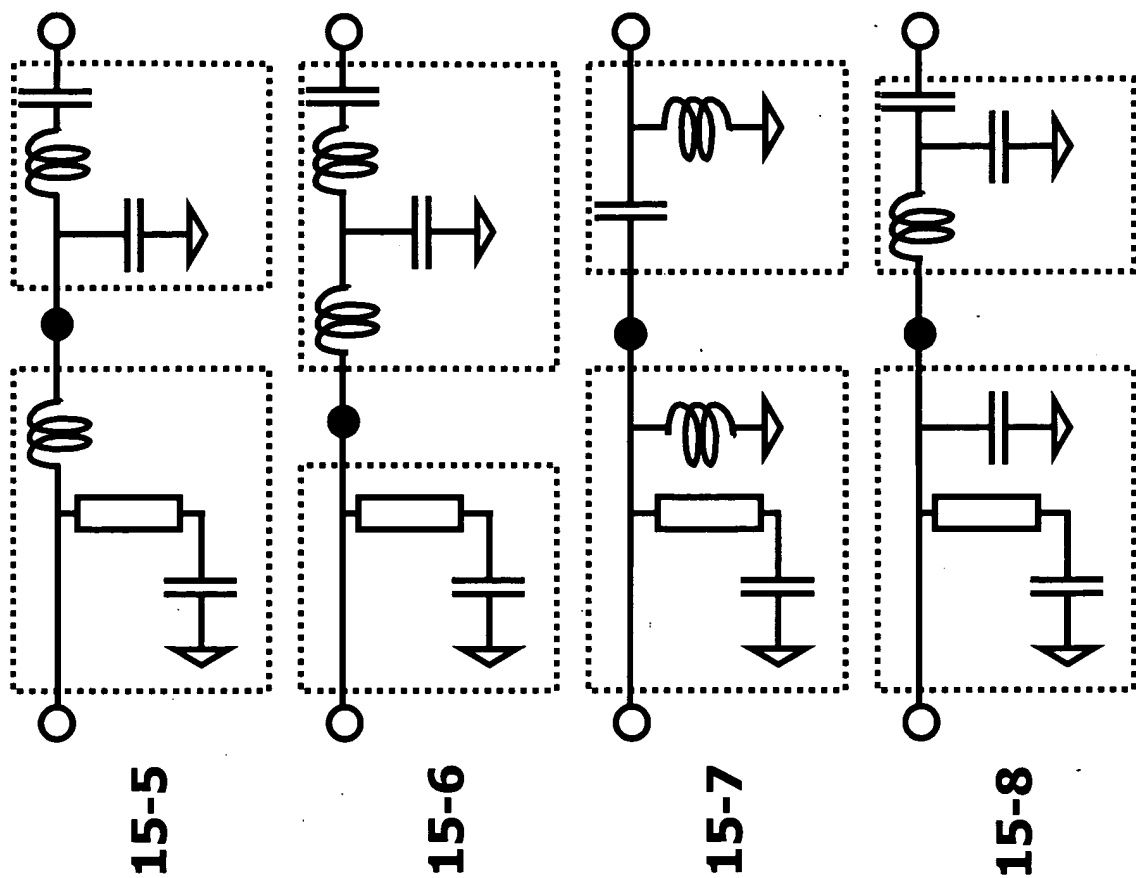
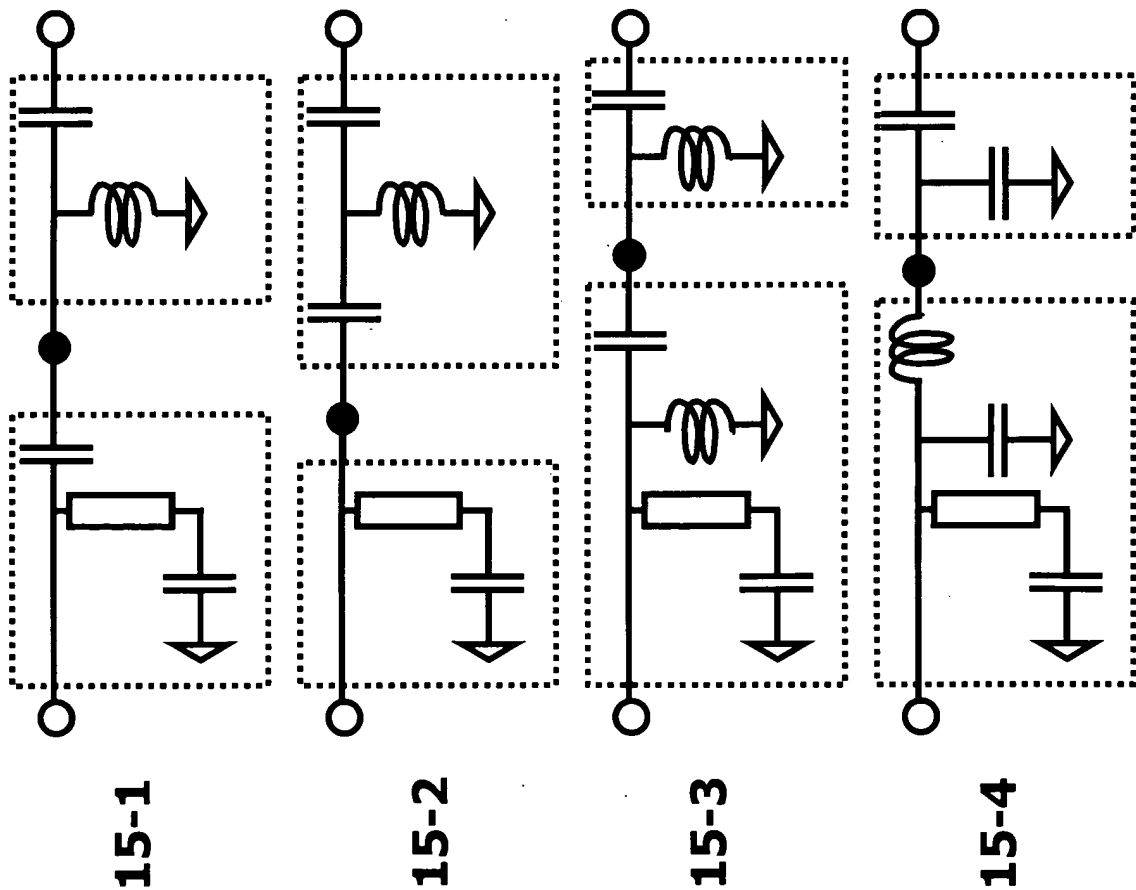


Fig. 15



A horizontal number line with open circles at 150 and 160. A solid black dot is placed on the line between 150 and 160, with a curved arrow pointing to it from the number 76.

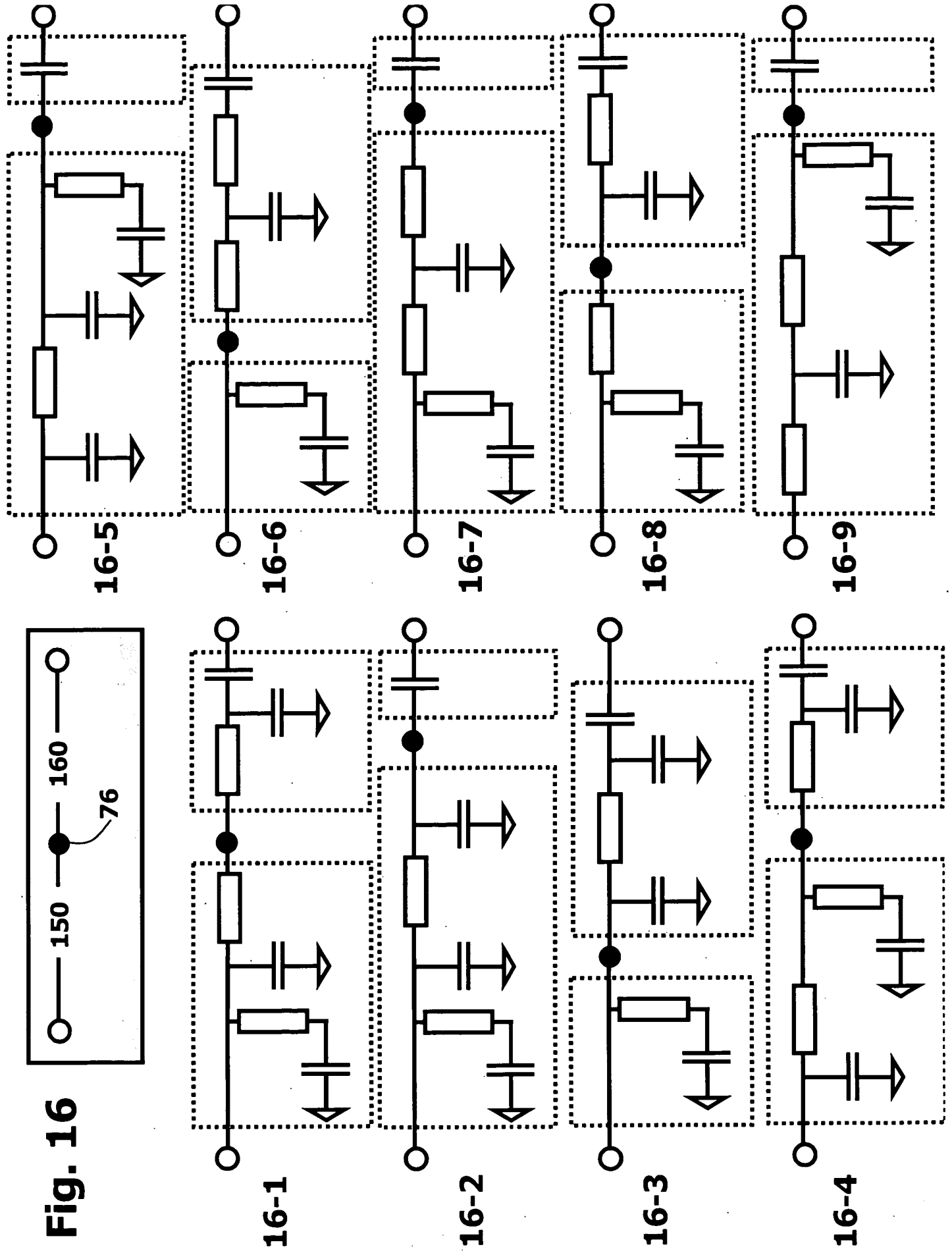


Fig. 17

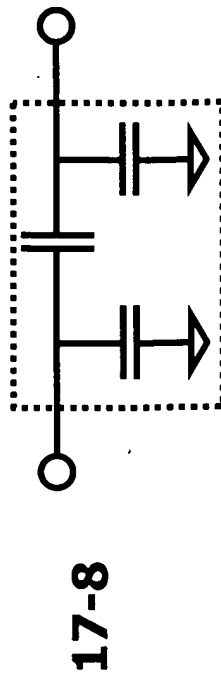
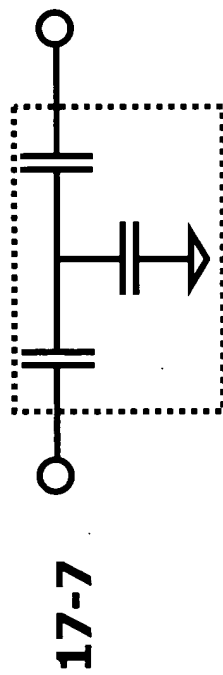
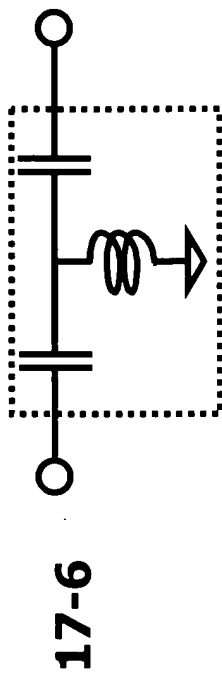
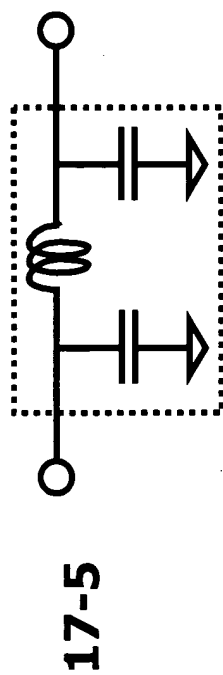
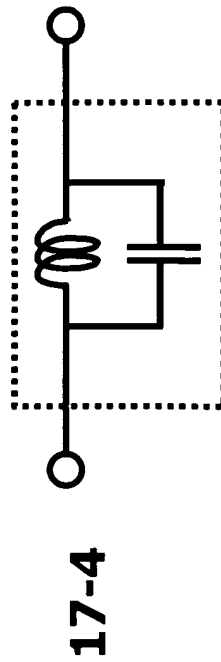
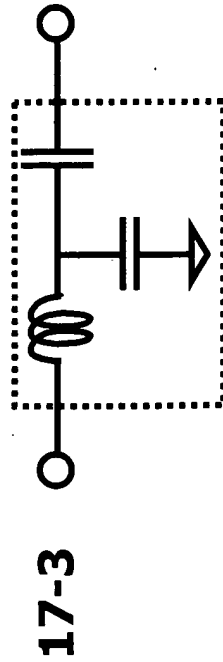
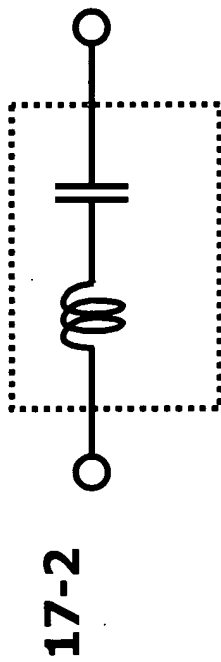
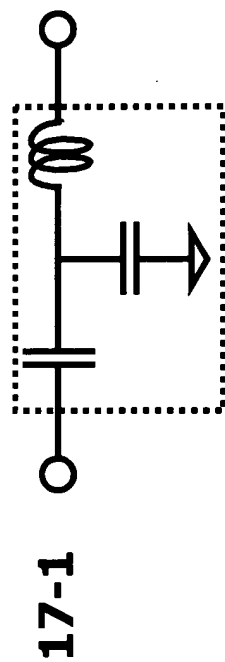
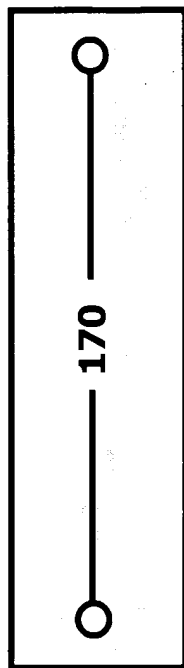


Fig. 18

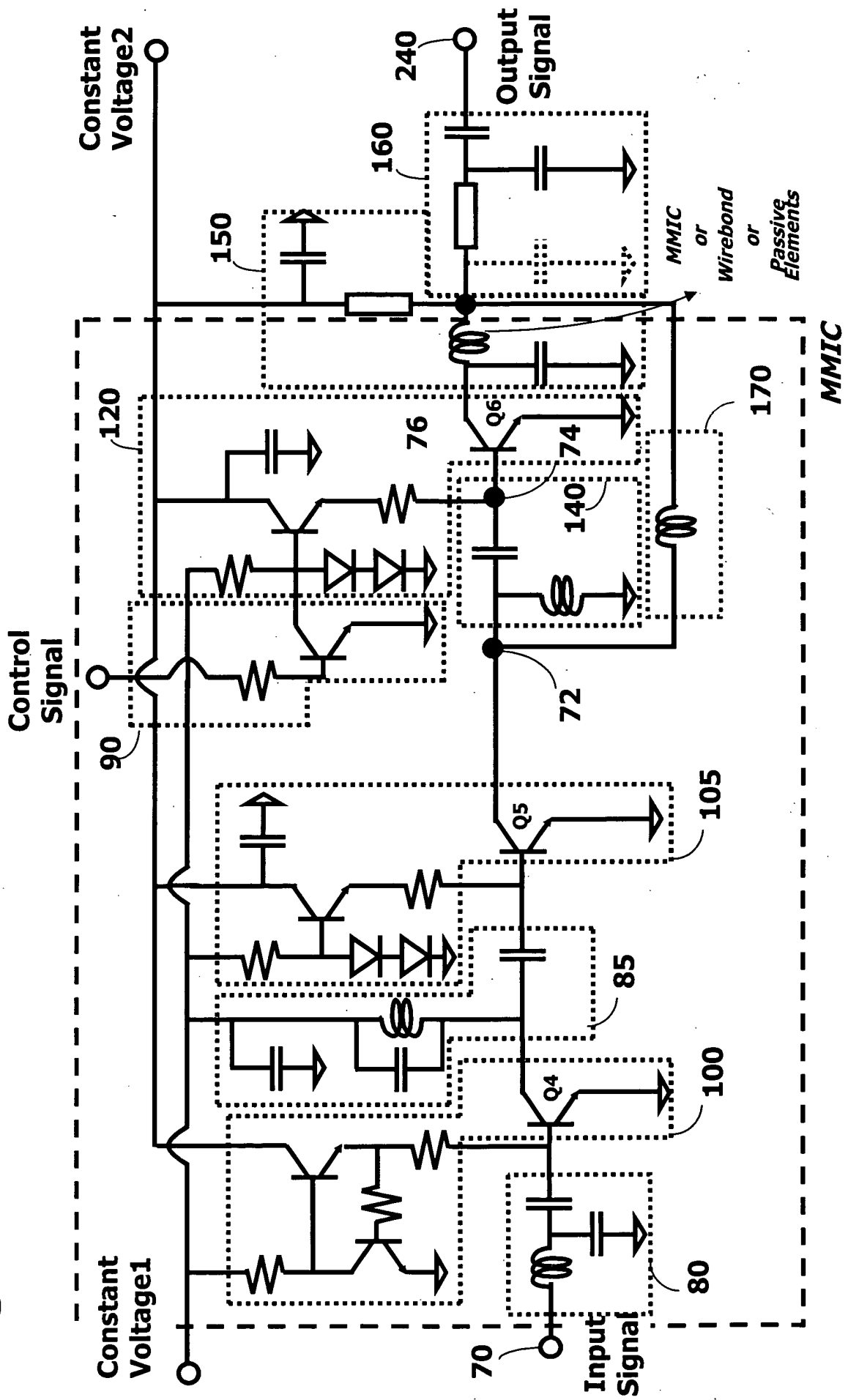
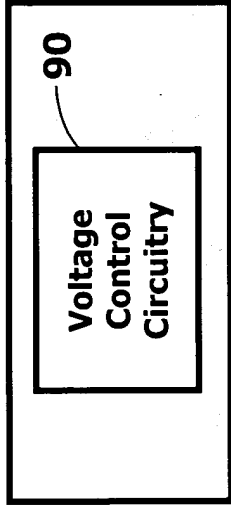
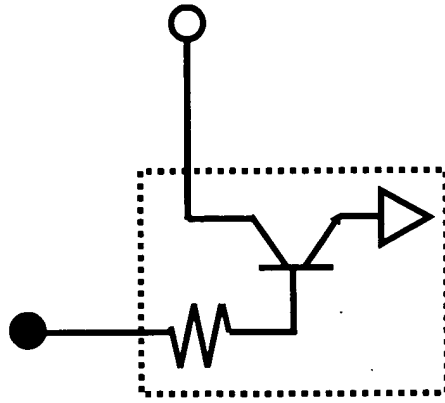


Fig. 19



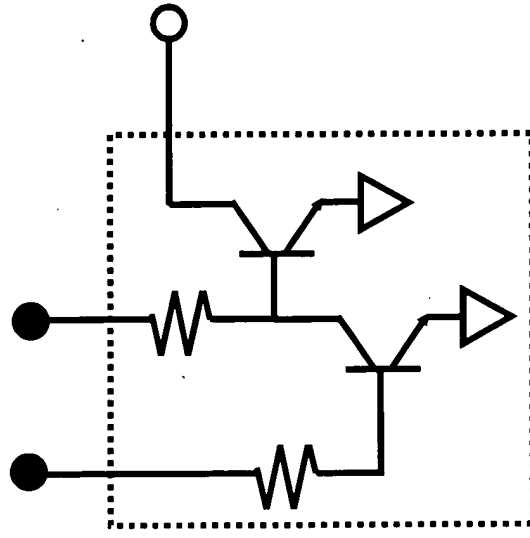
19-1

Control
Signal



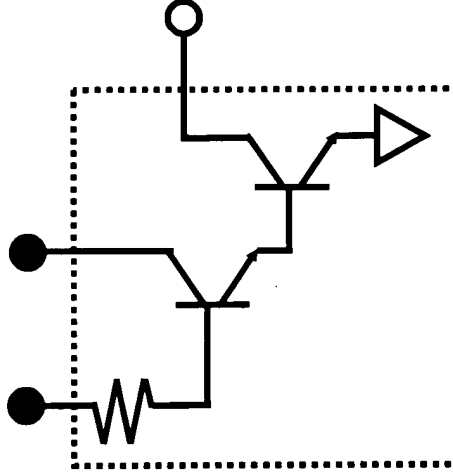
19-2

Control
Signal



19-3

Control
Signal



19-4

Control
Signal

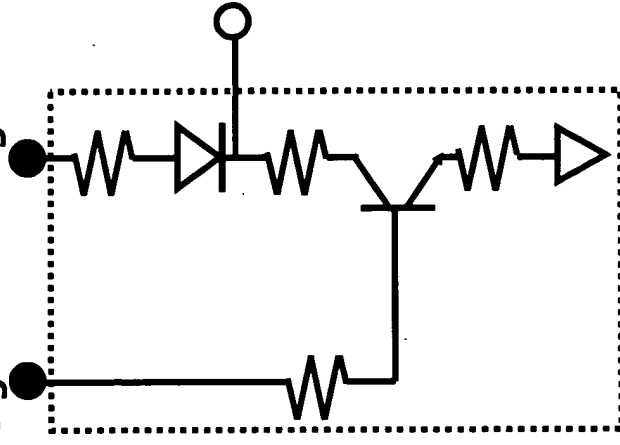


Fig. 20

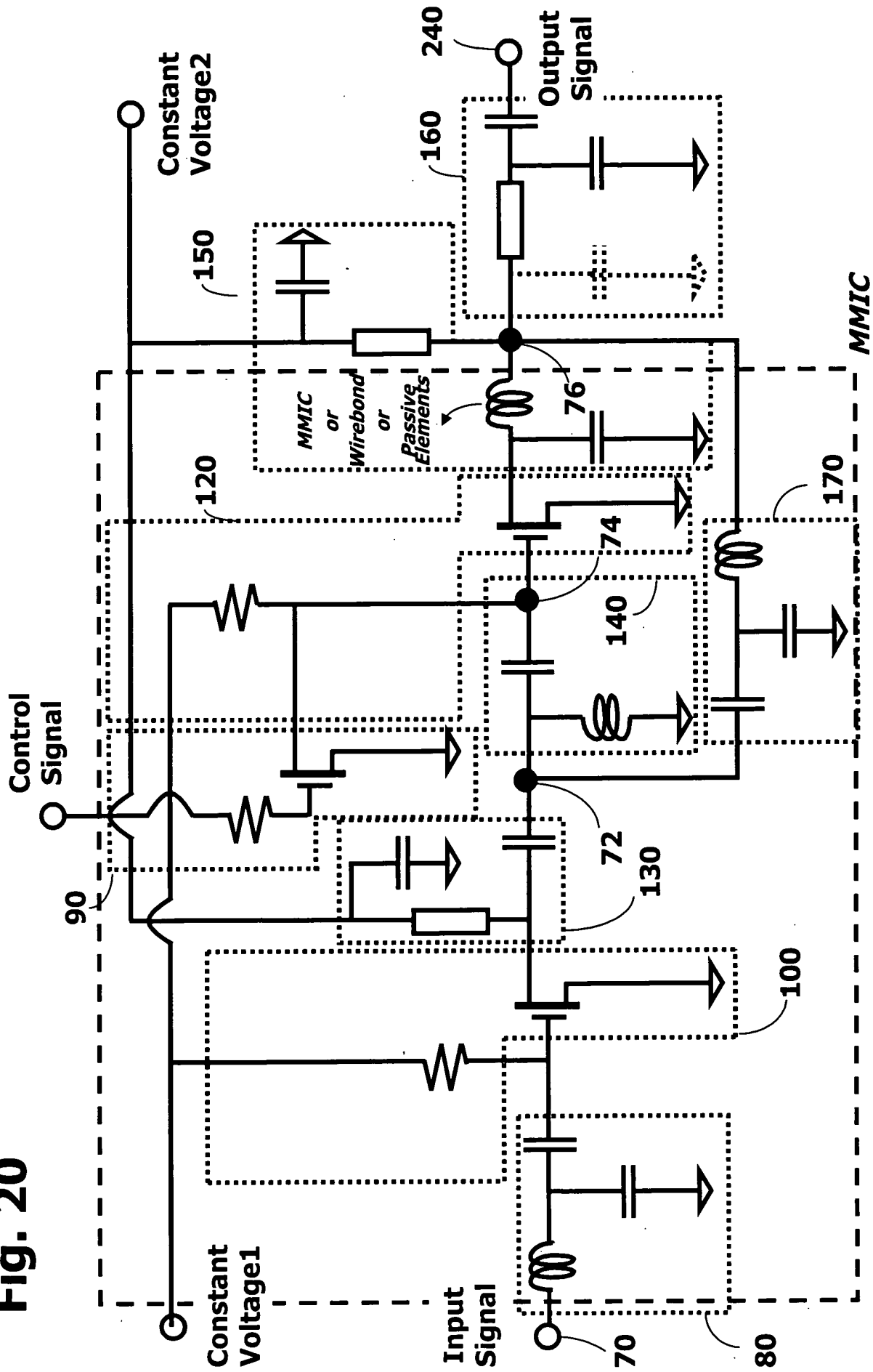


Fig. 21

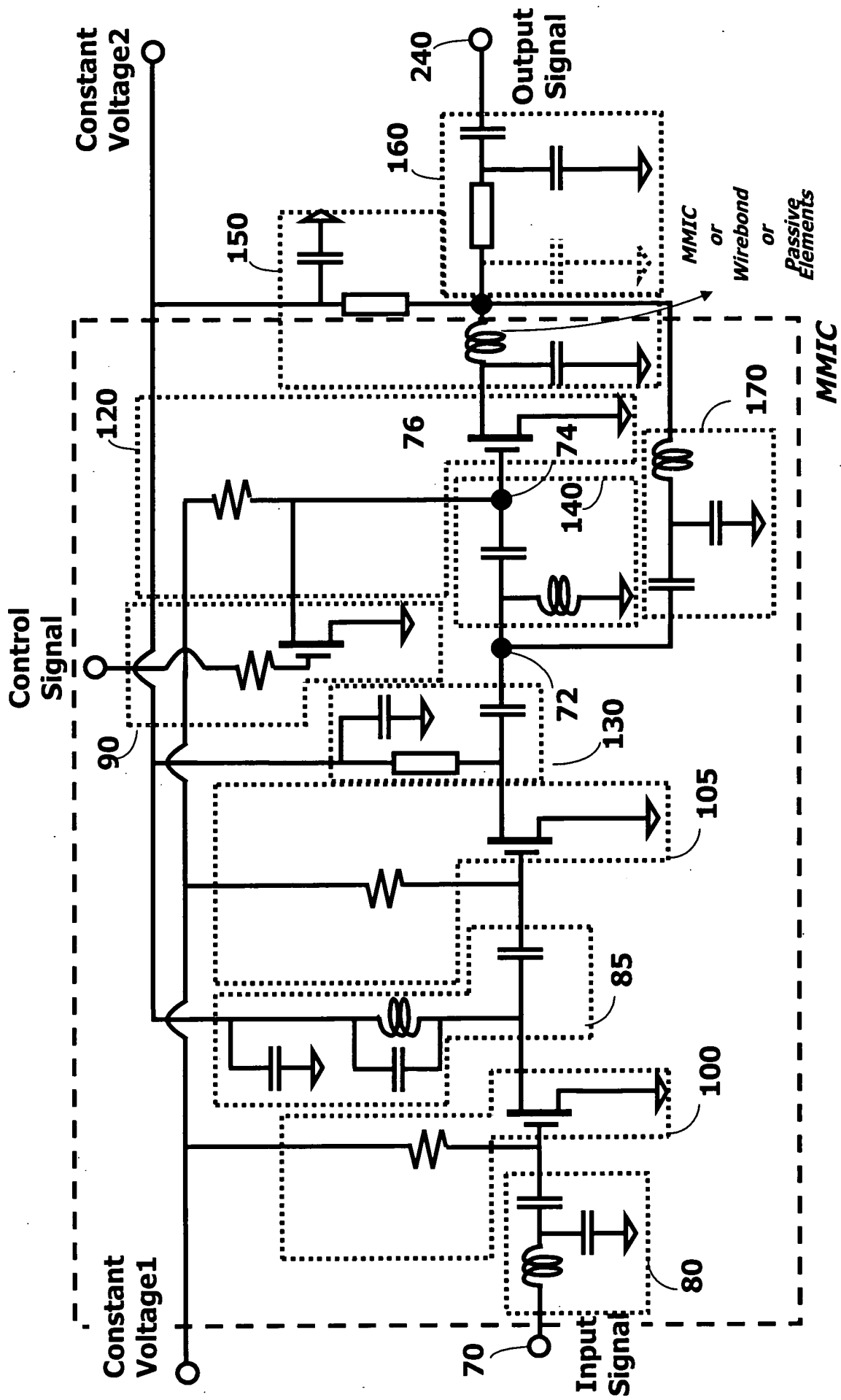


Fig. 22

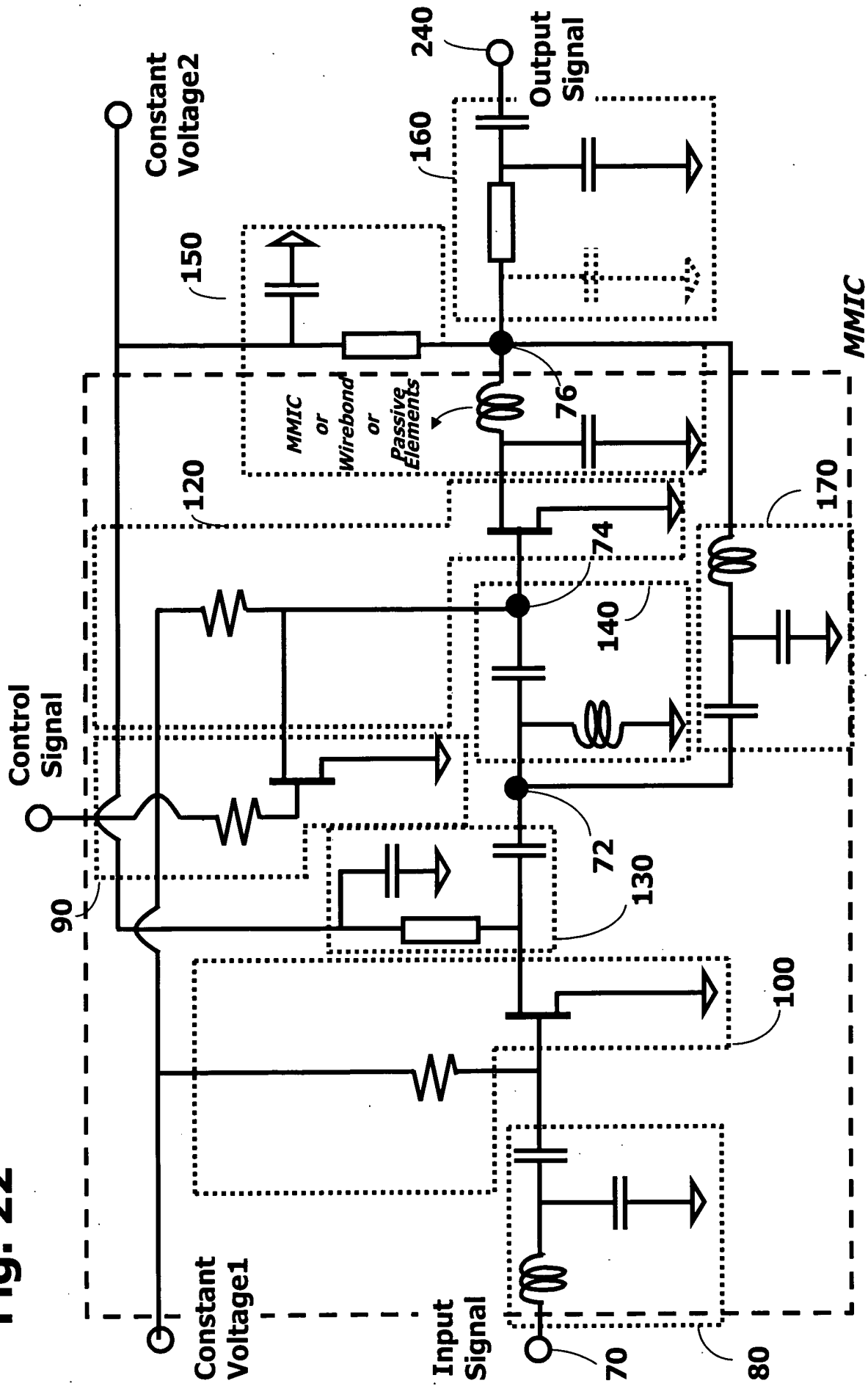


Fig. 23

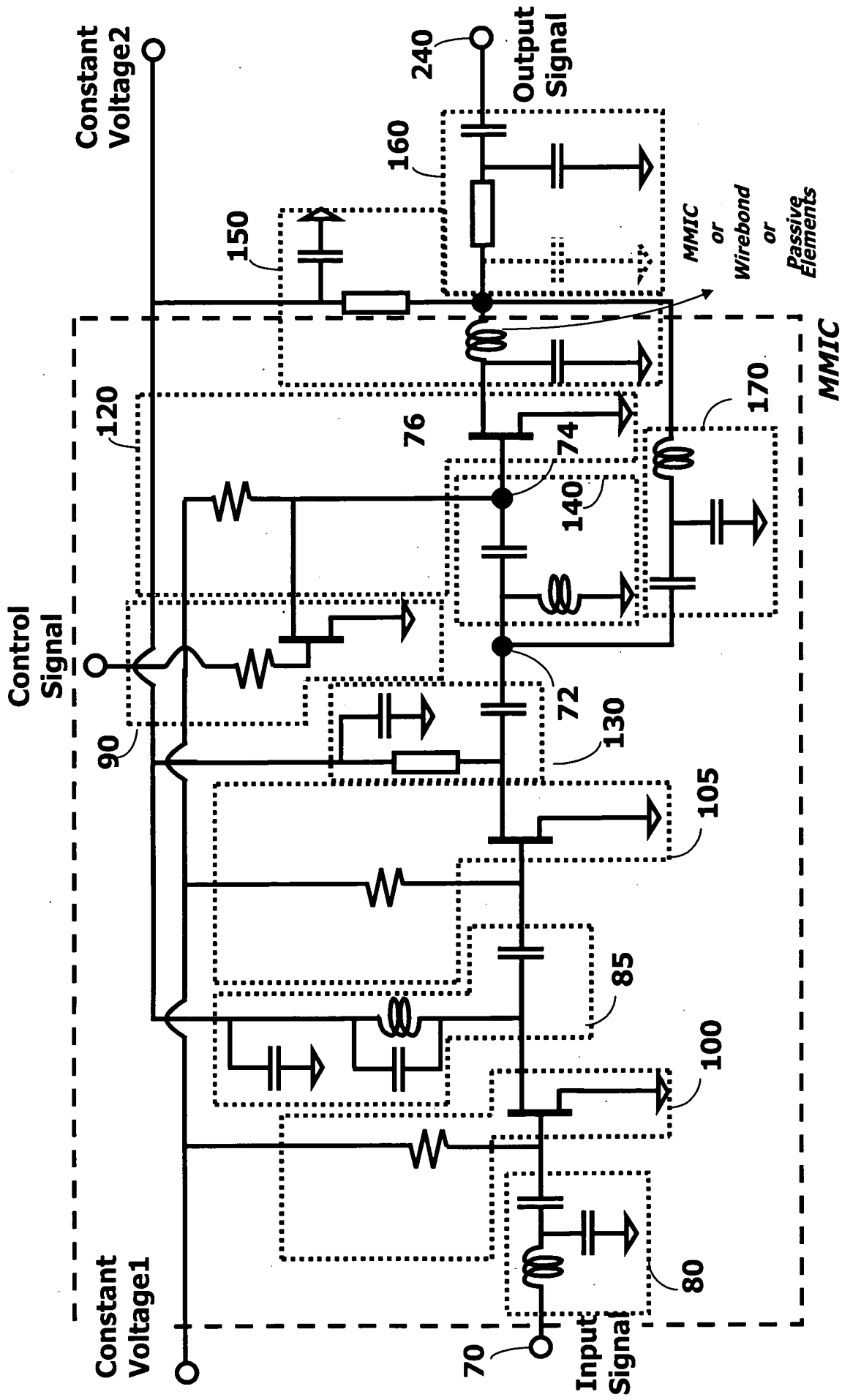


Fig. 24

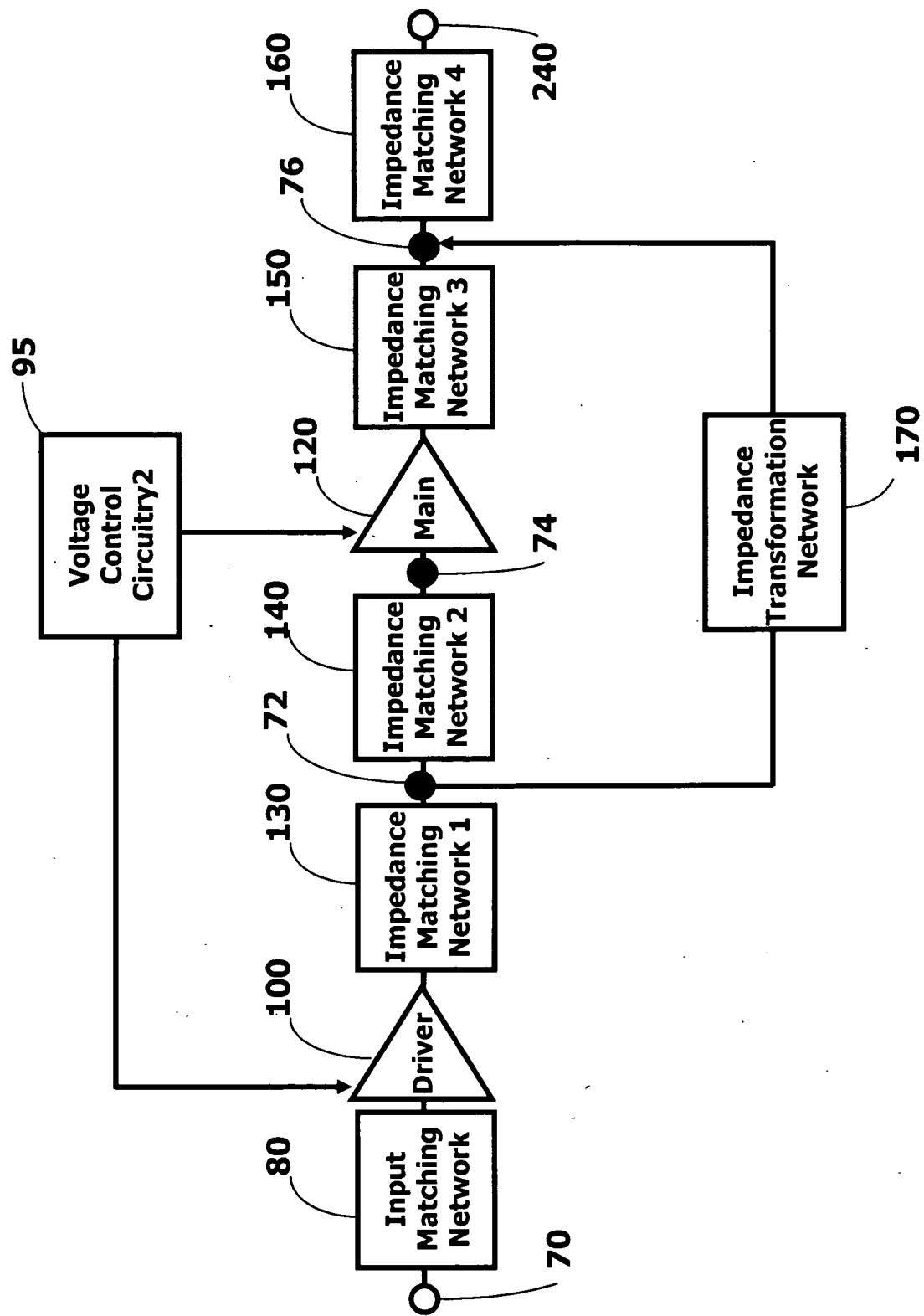
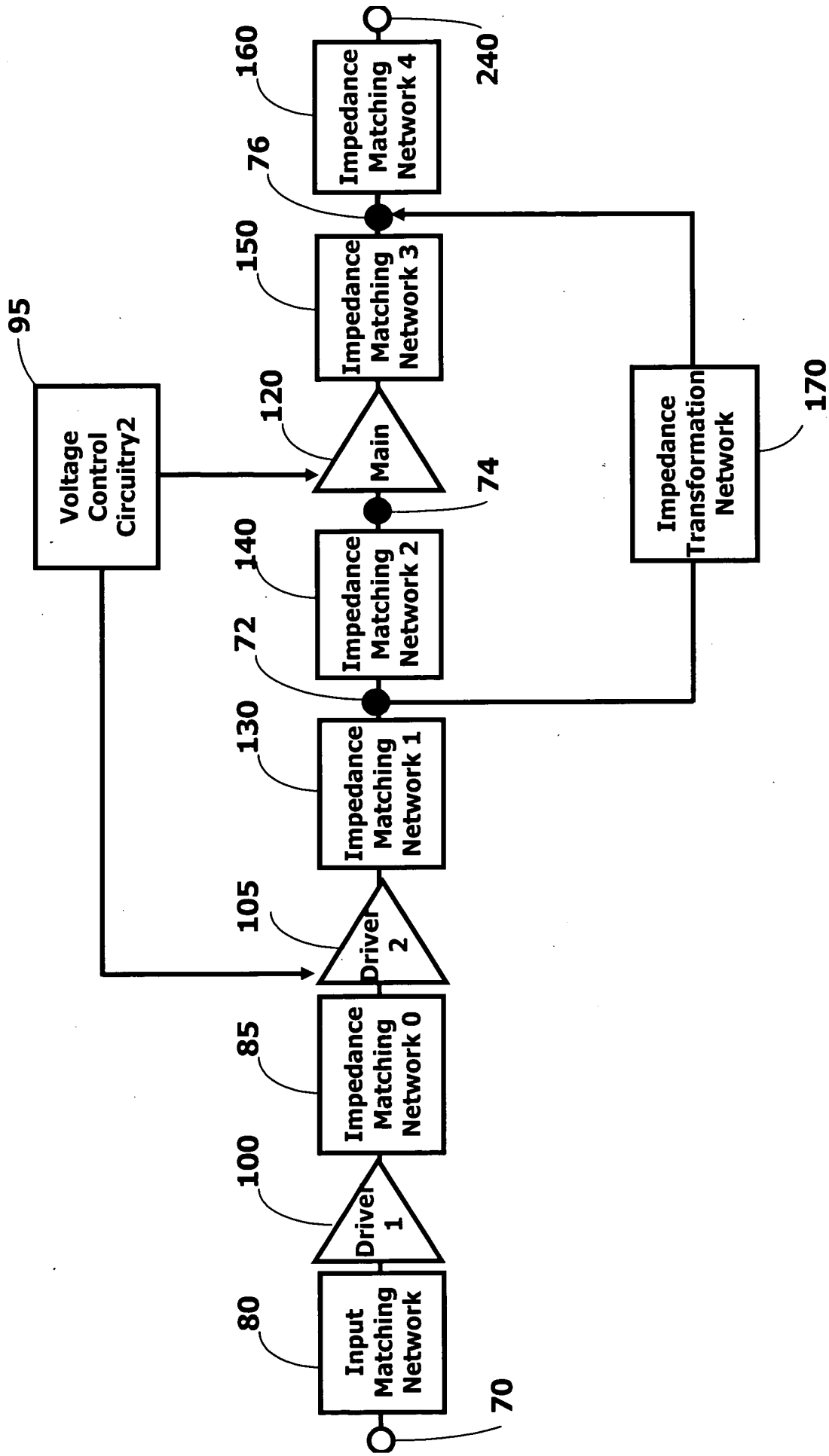


Fig. 25



Voltage Control Circuitry1 (90)

→ Basic Switching PAM

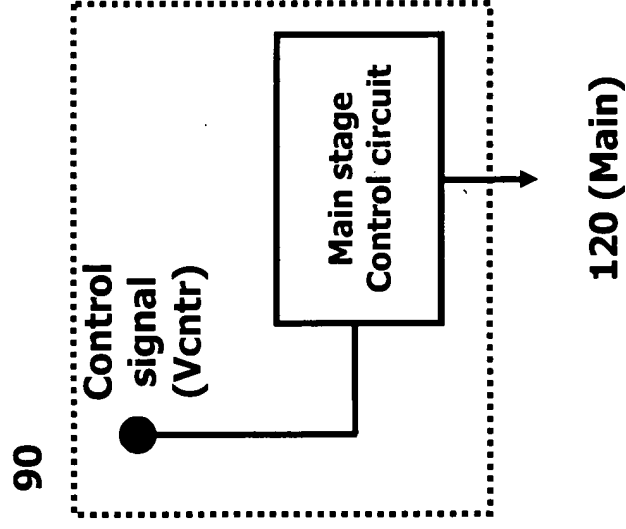


Fig. 26A

Voltage Control Circuitry2 (95)

→ Bias Modulated PAM

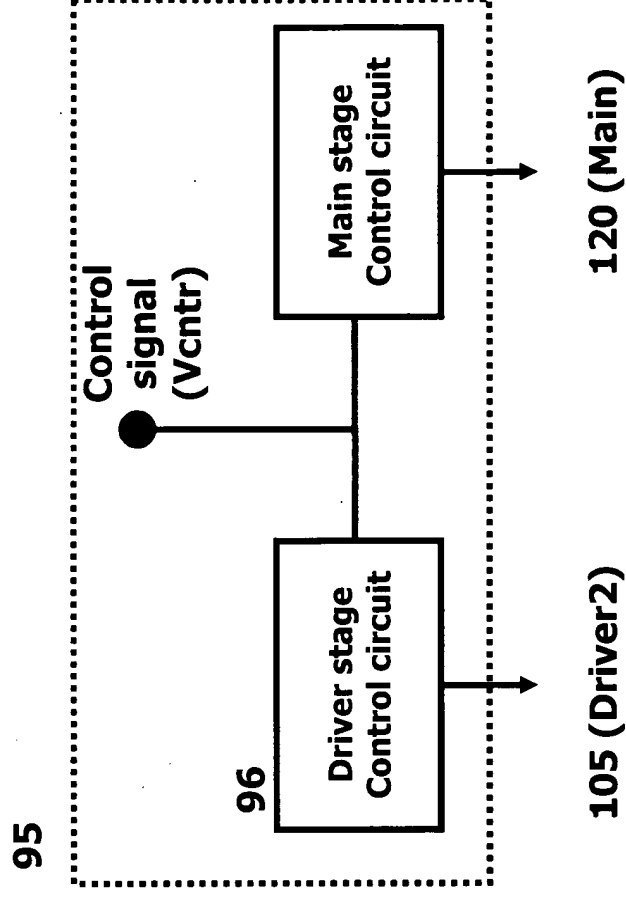


Fig. 26B

Fig. 27

Augmented voltage Control Circuitry2 (95) for Bias Modulated PAM

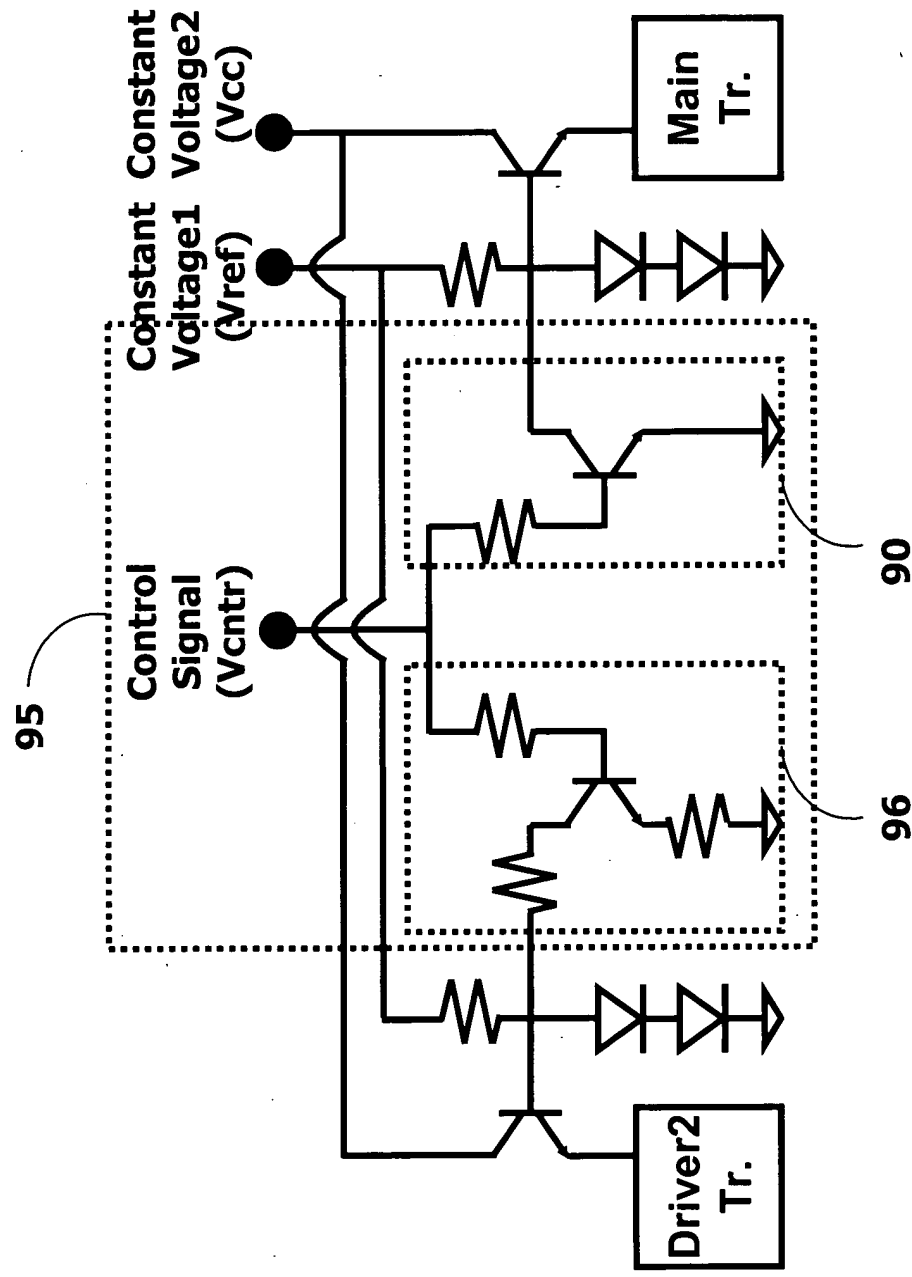
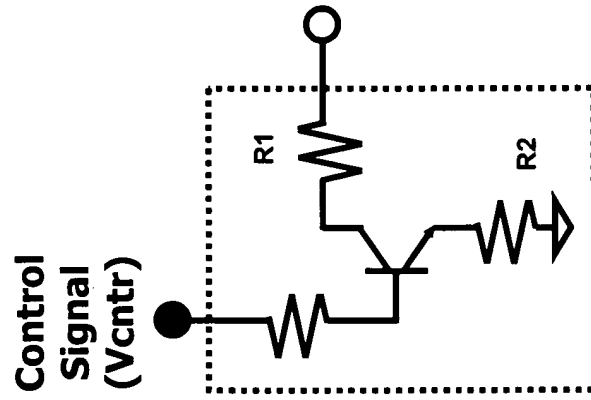
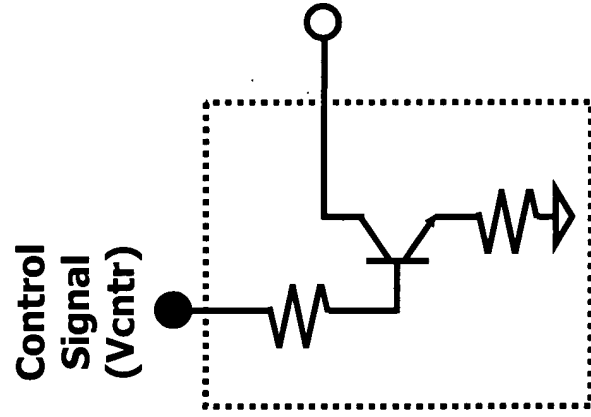


Fig. 28

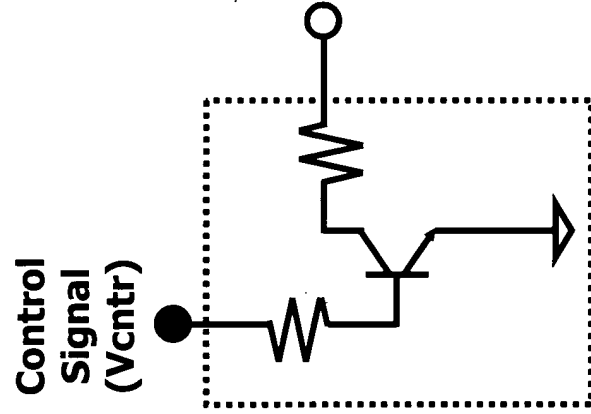
Examples of Driver stage Control Circuitry (96)



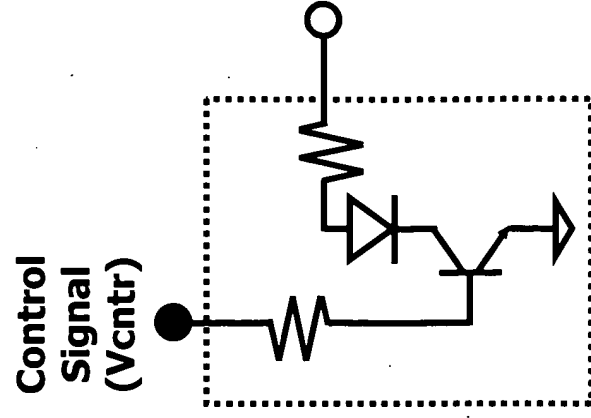
28-1



28-2



28-3



28-4

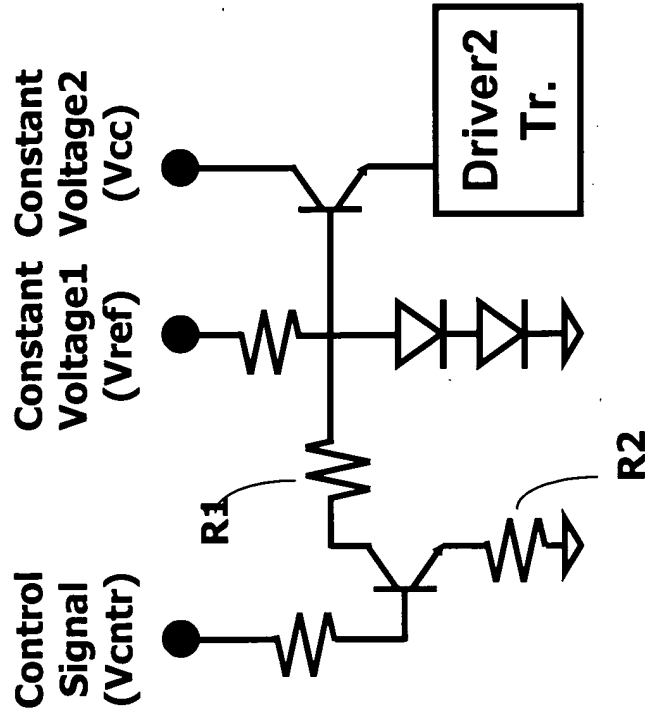


Fig. 29A

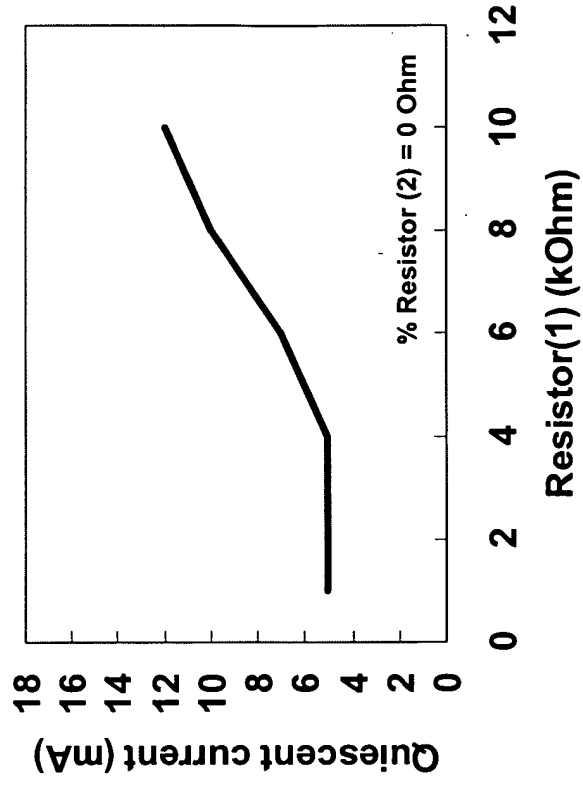


Fig. 29B

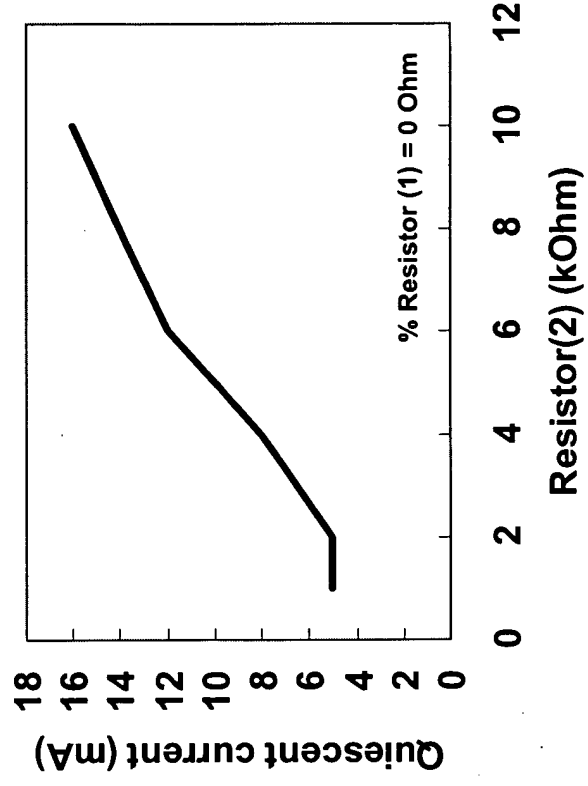


Fig. 29C

Fig. 30

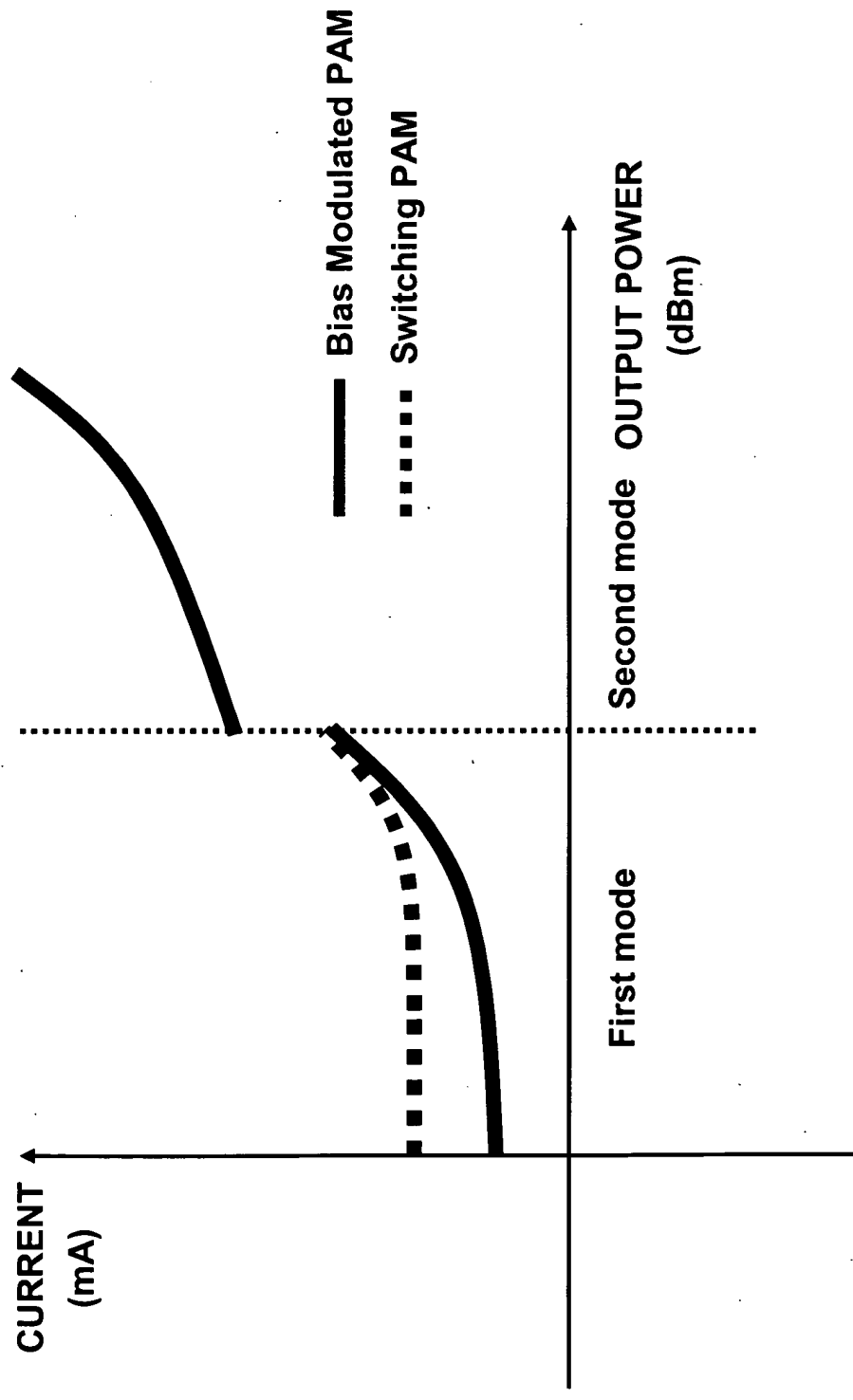


Fig. 31

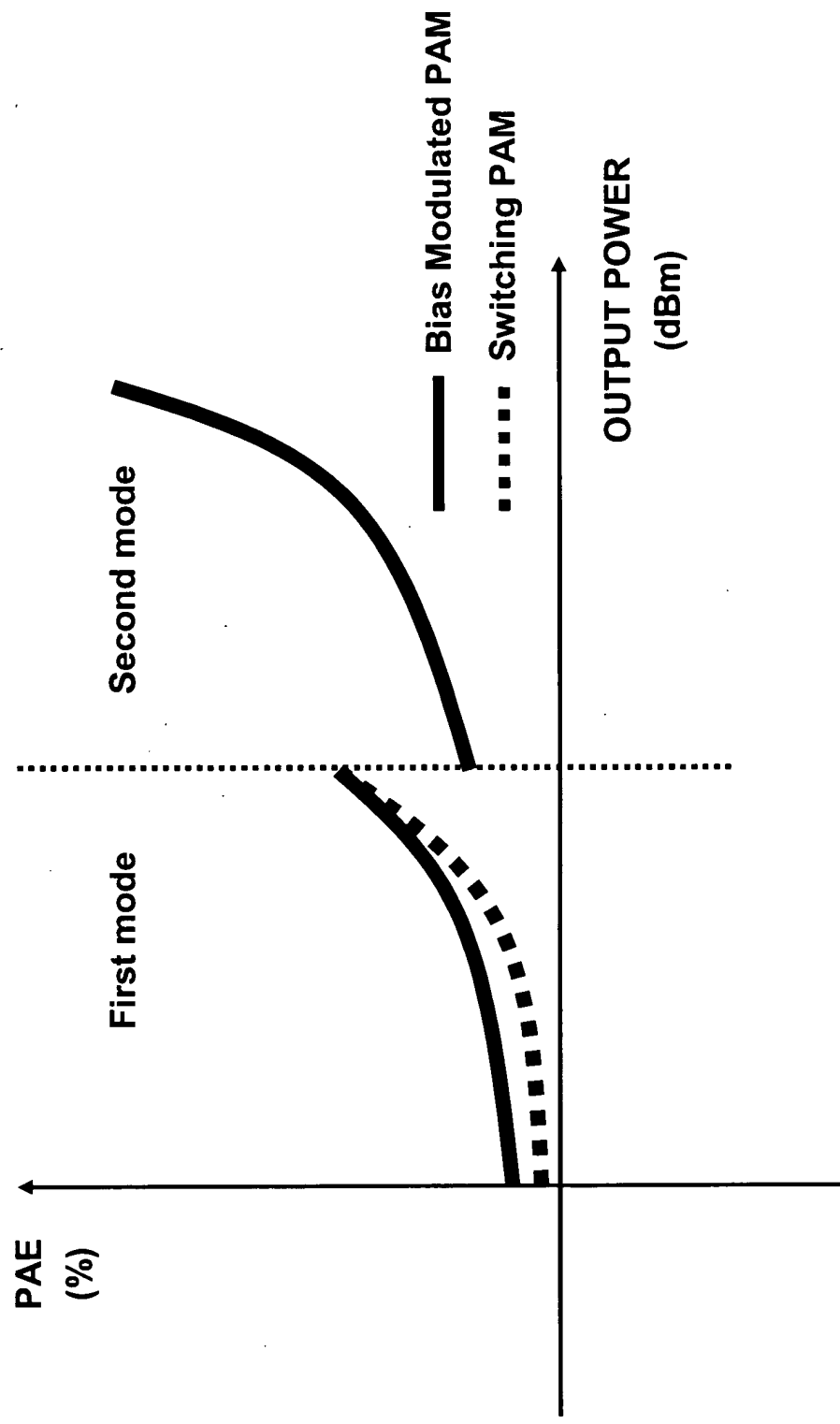


Fig. 32

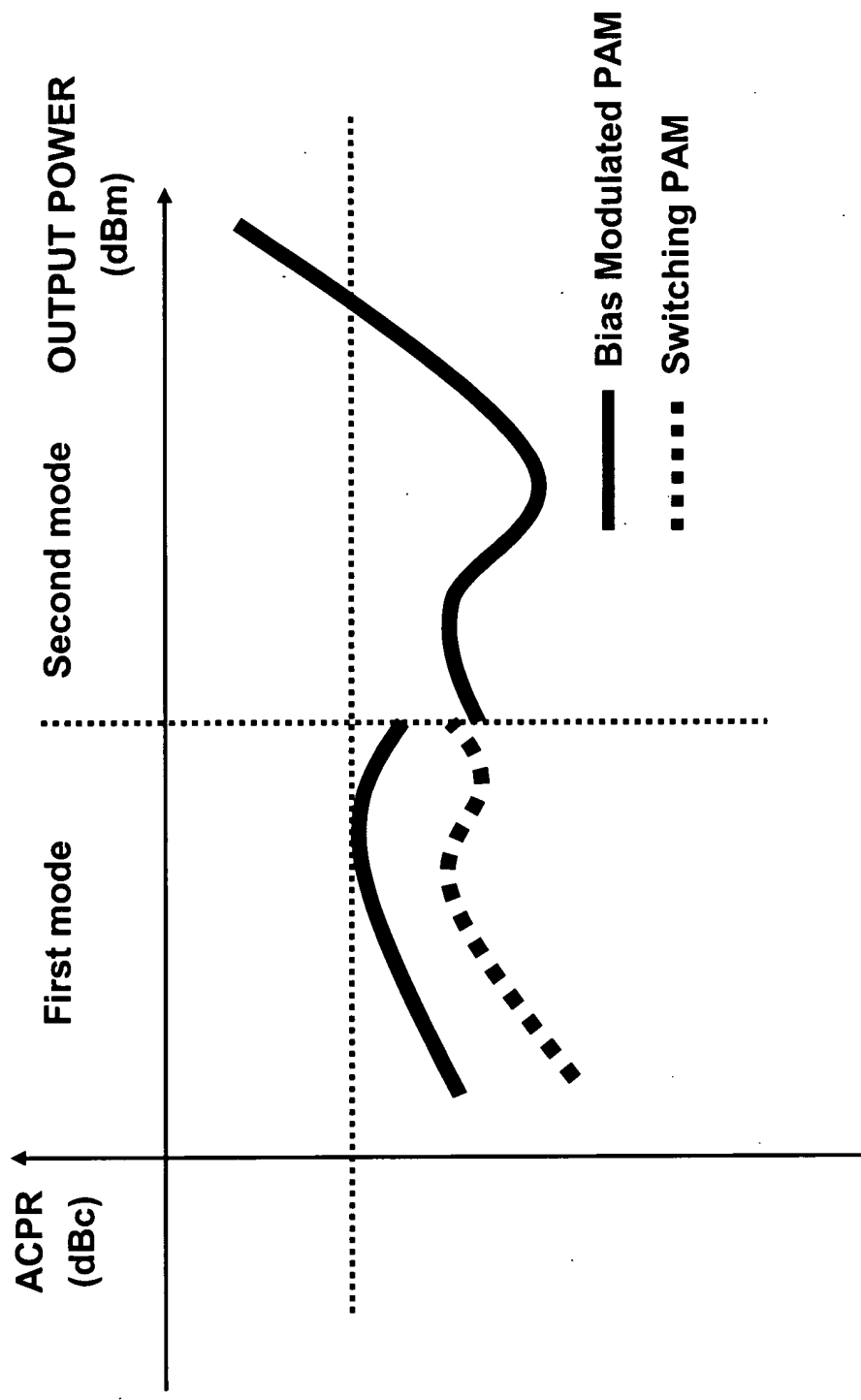


Fig. 33

